Innovative Detection of Isolation and Recombination Technique with Micro Grid

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Abstract—An innovative technique of the Unified power quality conditioner for the location, control and integration in the Distributed generation which is based on autonomous and grid connected μg system is proposed and simulated. At Point of Common Coupling, DG converters that has storage andshunt part of APFof UPQC are connected. Before the PCC, the grid and the series part of the UPQC APFare connected in series. Also the DC link is integrated with energy storage. As a secondary mode of control, an innovative technique is introduced in UPQC for the detection of isolation and recombination inµg. Henceforth, it is described as innovative detection of isolation and recombination technique. In this system an effectual method is used for proper functioning of the recombination and isolation detection. Along with the increase in the quality of the power in terms of voltage sag/swell, compensation of the reactive power and harmonics in recombination mode, additionally it compensates voltage interruption over normal UPQC and also DG converter remains connected during the conditions of disturbances in voltage and also phase jump. The performance of the UPQC is simulated and results are analysed.

Index terms – Distributed generation (DG), Micro grid (µg), Power quality, Innovative isolation detection & Recombination (IDR), Unified Power Quality conditioner (UPQC), Smart grid.

I. INTRODUCTION

The integration of distributed generation (DG), i.e., micro generation integration with unified power quality conditioner (UPQC) is successful and has some challenging issues which are primarily due to a) controlling the complexity of active power transfer capability, b) compensation of non-active power during islanded mode of operation, c) enhancement of capacity in integrated way . For unified power transfer between the isolated mode and also mode of grid connectionthat involves various changes in the operations like switching of voltage and current mode of control between them, robustness operation between the delay of recombination and isolation detection. [2,3]

Obviously, there is further increase in complexity for controlling the µg system. With an objective to improve the flexibility in operation and for the improvement of quality of power with μg and grid connected systems. An integration system of UPQC placement is proposed [3], and is termed UPQC μg.

For the maintenance of operation in both isolated mode and the recombination mode with UPQC, the connections between μg and UPQC as stated in [3], is implemented with intelligent isolation and novel technique of recombination with number of switches reduced which ensures seamless operation of Mg without its interruption, therefore it is known as UPQCμg -IDR. The benefits which are offered with the proposed UPQC μg-IDR with conventional UPQC as follows below

a) It compensates both voltage sag/interruption/swell, non-active current in the mode of interconnection. Even under distortion conditions of system, the DG converter is connected. It also improves operational tractability of DG converters or μg, which is explained in the next sections.

b) The APFis the shunt part of UPQC, which maintain association during islanded mode and also can compensate harmonic power (QH) , non-active reactive power of load.

c) In both the islanded and interconnected mode of operation Mg, active power is supplied to load. Therefore the controlling complexity of DG converters is reduced.

d) As secondary controlling of operation recombination technique and isolation detection are proposed. By using secondary control communication between μg and UPQC is provided. Isolation detection and the recombination feature may not be required by the DG converters in the control system.

e) UPQC-μg IDR can also works incondition of phase difference/ phase jump within the limits of grid and , and also have total controlling on isolation and detection and also recombination of continuous and stable operation of μg with best quality service of power.

The organization of the paperis followed as, The proposed system working principle is described in the Section II. Basing on this, issues of the design and selection of the ratings has been discussed in the section III. Detection of isolation and recombination technique are discussed in the section IV. The performance of the simulation is mentioned in the last section.

II. WORKING PRINCIPLE

The proposed technique of UPQCμg-IDRwith grid connected and DG integrated μg with grid and two breaker switches are used for isolation and also reconnection of the micro –grid to the grid as directions given by secondary control of UPQC μg-IDR. The principle and working during both modes of isolation and recombination are shown in the Figure. 1(b) and 1(c). The working of the UPQC μg-IDR will be divided into two methods of operation.
1. **Mode of Interconnected operation**

In the above mode of operation as given in Figure 1(b),

A) The load, storage and active power to the grid which is fundamental is delivered by the DG source.

B) For the maintenance of THD within IEEE standards and limits, shunt APF is used to compensate the harmonic (QH) and reactive power of the non-linear load.

C) The APFse is used to compensate voltage swell/sag/interruption of the active power from storage or grid. As any sort of disturbances in voltage at the PCC cannot be sensed by DG converters, therefore under any circumstances they remain connected.

D) If there is any black out or interruption occurs, the Distribution Generation converter receives a signal from UPQC in a specified time.

2. **Mode of Isolating operation**

In the case mentioned in Figure 1(c), the following occurs:

A) When there is a failure in grid, APFse is disconnected and Distribution Generation converter maintains voltage at PCC and remain connected in the system.

B) For the maintenance of undistorted currents at PCC, non-active power is compensated by APFsh of linear and non-linear loads.

C) Distribution Generation converter that has storage and delivers active power only and no necessity of disconnecting it from the system.

D) Once grid power is available, the APFse is reconnected to the system.

The switching mechanism in controller design is shown in Figure 1(a),(b),(c). UPQC micro-grid –IDR needs two switches when compared with UPQC µgin which four switches are required.

III. DESIGN AND SELECTION OF RATING

The representation of the fundamental frequency is shown in the Figure 1(d). The relationship between the current and voltage is derived in [1],[2]. According to working principle of APFse works during the voltage sag/well/interruptions up to some extent before it gets isolated. APFsh compensates the QH power of load. The selection and design rating of
series transformer, APFSe, APFsh and sizing of the dc link capacitor is also important and mentioned in the next section.

\[ V_{pcc} \leq \theta_{pcc} = V_s \leq \theta_s + V_{sag} \leq \theta_{sag} \]  \hspace{1cm} (1)

\[ I_{load} \leq \theta_{load} = I_s \leq \theta_s + I_{dq} \leq \theta_{pcc} + I_{sh} \leq \theta_{sh} \]  \hspace{1cm} (2)

A. APF(APFsh) shunt part of UPQC µg-IDR

APFsh compensates non-fundamental current of load at any condition by injecting the shunt current in quadrature with Vpcc. APFse injects the required voltage for compensating the voltage sag appeared on the supply side for maintaining constant voltage and also zero-phase atpcc. For the completion of it, additional current will be drawn by APFsh from source for supplying the power to APFse. The source current is in phase with Vpcc, even with the increase in its magnitude. But there is a change in phase angle and magnitude of compensating current Is as shunt compensator current will be added to the additional current of active component(s). Finally it increases current at the PCC and VA loading impact on APFshs created as mentioned in the [6]. So, it is

\[ I_s = I_{pcc} + I_{sh} \sin(\theta_{sh}) \]  \hspace{1cm} (3)

\[ I_{sh} = I_{sh} \cos(\theta_{sh}) \]  \hspace{1cm} (4)

B. APF(APFse) series part of UPQC µg-IDR

The Series APF and the grid are always in series. In the technique proposed for integration, if the energy is not obtained from the DG units and shunt part, APFse will compensate the reactive power and also harmonic component of the load current since fundamental component of load current will flow through APFse. To overcome this problem Series filter should have same rating of current as active component of load requirement.

\[ I_{APF_{se, max}} = I_{loadf} \]  \hspace{1cm} (5)

The generalized equations of voltage sag for compensation by series active filter are

\[ V_{sag} = \sqrt{V_s^2 + V_{pcc}^2 - 2V_s V_{pcc} \cos(\theta_s - \theta_{pcc})} \]  \hspace{1cm} (6)

The highest injected value of sag voltage must be equal to series active filter voltage rating and it is written as

\[ V_{APF_{se, rated}} = V_{sag, max} = KV_{load, rated} \]  \hspace{1cm} (7)

It is assumed that K is fraction of Vs, which reflects as voltage sag

\[ V_{sag} = KV \leq V_{load}, k < 1 \]

So, Volt Ampere rating value of series active filter is given as

\[ S_{APF_{se, rated}} = I_{APF_{se, rated}} V_{APF_{se, rated}} = K P_{load, rated} \]  \hspace{1cm} (8)

In the case Idg = 0, the active power through the series as shown in Figure.2 can be obtained

\[ P_{APF_{se}} = k P_{load} \frac{V_s}{V_{load}} \cos(\theta_s - \theta_{pcc}) \]  \hspace{1cm} (9)

During the operation of in phase and stable condition, it is assumed that\( \theta_{pcc} = 0 \)

\[ P_{APF_{se}} = \frac{kP_{load} V_s}{V_{load}} \]  \hspace{1cm} (10)

During this process of compensating the voltage sag, magnitude of the source current which is transferred through series active filter of the series transformer which is shown in the Figure.2(e) is obtained as

\[ I_s' = \frac{P_{load}}{(1-k)V_{dc}} = \frac{1}{(1-k)} I_{loadf} \]  \hspace{1cm} (11)

So, the VA rating and size of series transformer is based on magnitude of the sag which has to be compensated. As value of the k increases, the source current also increases. Basing on the information in [5]-and for assigned value of the k, a number of solutions for I’s, Vsag and PAPFse may exists. [5]-[7].

APFse rating of voltage is very important in designing the parameters as it governs some of the characteristics. They are range of compensation, the need for inclusion of storage devices and their size, series transformer overall rating.

C. DC Link Capacitor

During the occurrence of high swell or sag and interruption, APFse must operate before it leaves to isolated mode. At this instant, the abilities of DC link capacitor must be 1) The value of dc voltage and ripple in steady state should be maintained. 2) serve as energy storing element and also to supply non-active power of load as compensation 3) the active power differences between source and load should be supplied during the conditions of swell/sag/interruption. For some specific systems, it is very much better in considering higher values of Cdc such that it handles all the above conditions of operation. A better response of the transient state and lower steady state ripples are also obtained. From the calculation in [8], the size of the capacitor for proposed system will be as follows:

\[ C_{dc} = \frac{2S_{load} n T}{4. c. V_{dc}^2} \]  \hspace{1cm} (12)

Here Sload is total rating of load in VA and n is cycles required for performing the function, T is the interval of time and c is Vdc’s percentage.

It indicates compensation of capacitor size which is adjusted by selecting the cycles (n) and APFse. The purpose of proposed integration method technique in UPQC µg-IDR is for maintaining smooth supply of power during swell/sag/interruption and also extends flexibility of operation of DG converters during both modes of isolation and interconnected state. DG storage is introduced, for the continuity in supplying power. In the system proposed, a dc link is placed between the DG storage and capacitor. It also helps in decreasing the capacitor’s size and provides supply during interruption or condition of sag.
IV. DESIGN OF THE CONTROLLER

The proposed block diagram is shown in the Figure 4 which has the same functions as UPQC controller and has added functioning of reconnection capabilities and isolation detection. A channel of communication for signal transfer between μg and UPQC μg-IDR for smooth operation is required. The signals which are generated are based on swell/interrupt/sag/supply conditions of failure. The present function is carried out at second level i.e., control of hierarchy which is secondary [9]. First Level deals with control of the UPQC which is primary and performs the primary function in both recombination and isolated mode [10].

The integration technique along with control strategy is used for the improvement in quality of power during both modes of isolated and interconnected states. So, it involves detection of isolation and also reconnection which ensures DG converter remain connected and also supplies active power to load. Thus, decreases the controlling complexity of the converter along with possibility of power failure in isolated mode as mentioned in Figure 2(b).

A. INNOVATION ISOLATION DETECTION:

Besides isolation detection, to change the control scheme from current to voltage, the problem severity increases with the delay of control in isolation detection [15]. Therefore, smooth and continuous transfer of voltage between grid and isolated mode is significant [12,13]. Many controlling techniques are mentioned in [2], that increases controlling complexity for micro-grid converters [11]-[15].

Based on the requirements of isolation detection, recombination, interruptions, sag, swell compensation, with reference to Figure 2(a), (b), isolation is detected and signal Sµg –I is generated. In the process of proposed method UPQC μg–IDR a signal is generated for transferring to the DG converter. The proposed algorithm is IDR for UPQC μg-IDR and it is very simple and flexible. It helps in reducing the complexity of isolation and recombination detection of DG converters.

The algorithm used for the generation of signals is more simple and adjusted the value of Verror for any length of time specified. Here, flexibility in operational time and compensation of sag/interrupts control can be achieved before it gets isolated. Since there is continuous transfer i.e., voltage transfers the from grid connected mode to the isolation mode which is very critical task in period of transition and completes at zero-crossing position of the APFse, henceforth, there will be no voltage fluctuations or any impulse conditions.

The algorithm and the isolation technique developed in the control part of UPQC is very flexible and innovative in its operation. The operation and control of switches is important for both innovativeisolation, the seamless recombination which are shown in the Figure 1. In this case, the paper presents a step ahead when comparison with the use of intelligent connection agents (ICA) in [12], ICA and existing μg are connected with increased number of current sources which is an additional module. UPQC μg-IDR described in the paper performs the seamless transitions and also improves the quality of power with flexibility in operations. UPQC has series element (APFse) which performs a vital role of voltage source for μg, the observation of PCC voltage is based on anti-isolation algorithm and it is implemented as mentioned in Figure 2(b).

In conventional system, i.e., the systems which are grid connected with PV and in non-detection zone increases with number of inverters, as they cannot distinguish between PV inverter or external grid output voltages. So, they remain in connection for a long period of time which is dangerous. In this proposed control strategies of UPQC, an existing PV plant can be added and it is only responsible for the support of voltage and isolation detection. So, it has more
effectiveness and reduces drastically non detection zone.

B. SYNCHROIZATION AND RECOMBINATION

Once re-estabishment of the grid is occurred, µg can be recombined to the main grid and returns to the condition before its interruption. A very smooth recombination is obtained if the difference between phase, voltage magnitudes, frequency of two buses which are closer to zero or minimized. This seamless recombination depends on the performance & accuracy of the methods of synchronization [18]. APFse performs recombination in the case of UPQC µg-IDR. Because sag/swell are controlled by APFse, it has the advantage of recombination of the system even if phase difference or jump to some extent between utility and PCC voltages. This flexibility in the operation is automatically increased with high quality of power in µg. The limitation of phase difference is based on APFserating and level of Vsag-max for the compensation.

By assuming the value of Vsag-max= Vse, Vsag, the θsag-max is obtained as

θsag-max= cos[(θs–θpcc)]-1= 1/2 = 60°(13)

The relations of the magnitude and phase difference between Vpcc, Vse, Vsag are given in Figure 6(a). This Figures shows also zero-crossing point of Vsag-refbasing on the phase. When the instantaneous difference of voltage between utility and PCC becomes zero crossing is also detected by it. By detecting zero-crossing point and operation of switches S2 and S3 as shown in Figure.1, are key control of the recombination method for continuous transfer from off-grid to on-grid condition and also changing controller of DG inverter from voltage to the current control mode.

This method of recombination is shown in the Figure.4(b) and the conditions of recombination are as follows: 1) It is assumed that the phase difference must be within θsag-max between DG and the utility grid. 2) The instantaneous values of both the bus voltages will be equal 3) they must occur at condition of zero-crossing. After blackout, once the utility supply is restored, a synchronization pulse which is generated in the process of recombination will be enabled to start the synchronization. Basing on the conditions shown in Figure.3(b), a simple logical sequence generates active pulses for the switches S2 and S3 for returning the system to interconnected mode. At this instant UPQC µg-IDR, as represented in the Figure.2(b) is transferred to µg for recombination.

One of the advantages of IDR and Synchronous recombination method is carried in level 2 as secondary control, they can be added as additional block in conventional UPQC to convert to UPQC µg-IDR. In the proposed UPQC µg-IDR, it is very helpful in meeting the integration of grid advanced features as specified in the [18].

V. PERFORMANCE STUDY & RESULTS

With the drastic improvement in technology, the system is developed and modelled in Matlab using simulation software for observing the performance.

An active distribution network with 3-phase, 3-wire line Voltage 230v, with proposed UPQCµg-IDR as shown in the Figure.1 is simulated in software.

The specifications of the simulation developed are as follows: UPQCµg-IDR with a sag of 100%, harmonic currents compensation of maximum 100A and the Micro grid with load of 200A max, harmonic current 100A max, DG has 0.5 to 1.5 times of fundamental component of load. Simulations in the MATLAB are performed up to 2 sec for observing complete performance of system.

In Figure.3(a) it shows the switching positions of (0 for open and 1 for close) during simulation for a period of 0 to 2 sec in which both the modes of islanded and interconnected are observed. The proposed system of UPQCµg-IDR’s voltage sags performance is given in Figure.3(b) and compensation of harmonic current is given in the Figure.3(c). The condition of sag occurs at 0.3sec to 0.7 sec and sag interrupt till 1.1sec. Islanding operation takes place from 1.1sec to 1.4 sec and recombination mode occurs at 1.4 sec to 2.0 sec. Generally, all waveforms of phase A are only shown in the outputs.

A. Mode of interconnected operation

In interconnected mode, there are two possible operations which are forward flow and reverse flow. In this forward flow, load demand is larger than the DG power available. Therefore, the balance power to load is supplied by utility. When the load demand is lower than the generated DG, power is transferred to grid and also to storage and the process is known as the reverse mode of operation. At this instant, voltage at the PCC will be out of phase with grid current.

B. Mode of Islandation Detection

In accordance with the µg-IDR series active filter which compensates sag upto 0.6sec which is 30 cycles and now system changes into isolated mode. Now disconnection of utility at instant of 1.11sec is applied immediately after completion of the 30 cycles and it detects zero crossing of Vsag-ref which leads to opening of S2 and S3. At this instant of disconnection,µg will operate in the isolated mode. Now, if load demand is larger than the DG power available then the required excess power is supplied by the storage. If load demand is lesser than the DG power available, excess power is supplied to storage for charging. Here compensation of the non active power is still performed by the APFsh. Hence, the disconnection of the DG converter or changing of control strategy is not needed and supplies only active power to load. In Figure.7 the proposed system of UPQCµg-IDR’s performance at instant from 1.0 sec to 1.2 sec, here isolation mode is detected at 1.1 sec immediately after zero crossing detection. Now, at time instant between 1.11 and 1.405 sec mode of isolation is observed. Now, series active filter is disconnected at these instants which is shown in the Figure.4(b) and Vsag, Is are zero which is shown in the Figure.4(c).

The load is supplied by both the storage and the DG and shunt active Filter operates continuously as shown in the Figure.4(c).
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Figure 3. (a) positions of switches during modes of operation (b) waveforms of voltage (c) current with different conditions of operation
C. Mode of Recombination

The signal for the process of reconnection are shown in the Figure. 8. For checking its performance and considering one of worse condition, utility gridVs at an instant of 1.4 sec is powered and 400 out of phase from the PCC. Now the recombination process is activated immediately and generates active pulses when the condition of amplitude difference and phase magnitude were within the prescribed limits the switches activated are S2 and S3 at 1.415 and 1.405 sec. As shown in the Figure. 5(a) APFse reactivates immediately and start its operation if Vs is obtained and S3 connects at 1.405 sec as shown in the Figure. 5(b). Once S2 is closed at 1.415 sec then transfer of power starts as indicated in the Figure. 5(c). It is also expected, there will be no transfer of power at the instant of reclosing according to condition of smooth reclosing. Within the conditions of limitation the switching processes is carried successfully and shown in the Figure. 5(b). There is also a change in control mode from voltage to current in DG and transfers only fundamental active current component. Shunt APFsh also interrupted during the process of transition.

VI. CONCLUSION

The present paper analysed a powerful UPQC μg-IDR controller for the Isolation and Recombination technique. The proposed UPQC innovative detection and recombination in both grid connected and μg conditions and the results shows that it will compensate the current and voltage disturbances at PCC during recombination mode also and it is observed in bidirectional condition of power flow. The performance
analysis in bidirectional power flow is also observed. The active power is supplied by the DG converter only in the isolated mode. The operation of the µg in any operating mode and at any instant, DG converters need not be disconnected or no necessity of changing in control strategy. The bidirectional flow of power and dynamic changes in the Innovative Isolation, detection and recombination technique by using UPQC µg- IDR is validated with Matlab environment without compromising any of the quality of power for a distribution generation integrated with µg system.

REFERENCES