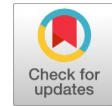


Device Modeling with Memristor Element for Chaotic System

Savarimuthu Prakash, Sam Thomas



Abstract - The proposed circuit is a basic electric circuit that produces chaos which consists three energy storage devices, a dissipative device, and a non-linear device. This paper investigates the use of a memristor as the nonlinear device in Chua's circuit. The memristor is represented mathematically by the port equation and the state equation. The memristor state is defined by the past and present values of input voltage rather than by a piecewise linear or cubic polynomial function. The proposed circuit shows that the circuit undergoes chaotic oscillations that are sensitive to the initial conditions.

Keywords: Memristor, Chaos, Nonlinear dynamics, Chaotic circuit, Chua's circuit

I. INTRODUCTION

In electronics are only three fundamental two terminal passive components, which is a dissipative element (resistor) and two energy storage elements (capacitor and inductor). The devices are defined using the circuit variables (voltage, current, charge and flux). Chua [1], in 1971, mathematically postulated a fourth fundamental circuit component relating both charge and magnetic flux. The nanodevice has the property of remembering the last applied input. The memristor is an electroic device (or a class of memristive devices) that changes its memristance based on the amount of charge flowing through it. The memristor acts as a resistor which has memory property and exhibits fascinating non-linear characteristics. Chua's circuit has an inductor and a capacitor forming a tank circuit connected in series via a resistor to another parallel combination of a capacitor and Chua's nonlinear device, as represented in Fig. 1. Thus, a new Chua's chaotic circuit may be constructed using three energy storage components and an active Chua's diode for generating chaotic output. Chua's circuit has applications in secure communication, chaos control, and image processing[2-4].

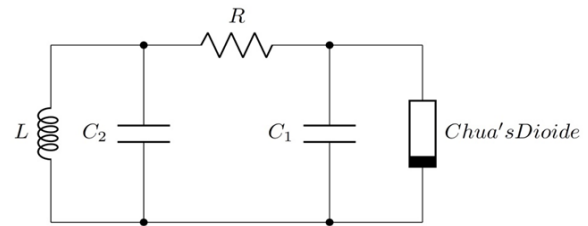


Figure 1: Chua's chaotic circuit.

II. LINEAR DRIFT MEMRISTOR AND CHUA'S CIRCUIT

The linear drift memristor model put forth by HP has TiO2 and TiO2-X positioned between platinum electrodes (Figure 2). A voltage with a positive bias applied at the pt electrode on the TiO2 side repels all of the oxygen vacancies which are positively charged. Thus, the doped region (TiO2-X) widens (w increases) and the conductivity increases (device resistance decreases). When a voltage with negative bias is applied at the same terminal attracts oxygen vacancies which are the positively charged, reducing the TiO2-X region (w decreases) and the conductivity (device resistance increases). As the TiO2-X region grows, the width of the TiO2-X region (w) approaches the thickness (D) of the entire device, the resistance reduces, and the device is considered to be in the ON state. As w increases to the value D, the resistance of the component will be RON which is the resistance of the doped (TiO2-X) region. As the doped region shrinks (or the TiO2 region expands) and the width of the undoped region approaches the thickness D of the entire device, the resistance increases, and the device is considered to be in the OFF state. ROFF is the value of the TiO2 region when w = 0 [5].

The memristance of the device is defined by using a series combination with a slider combination [6] in between the resistance of the TiO2-X region (RON) and the resistance of the TiO2 region (ROFF), as given by the port equation:

$$v(t) = \left[\frac{R_{ON} * w(t)}{D} + R_{OFF} \left(1 - \frac{w(t)}{D} \right) \right] i(t) \quad (1)$$

The values of both doped region resistance and undoped region resistance are controlled via the ratio w/D. The rate of change of w is proportional to the current i(t), as given by the state equation

$$\frac{dw}{dt} = \mu_V \frac{R_{ON}}{D} i(t). \quad (2)$$

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where dopant mobility is represented as μ_V .

Memristors exhibit pinched hysteresis. For a sinusoidal input, the current-voltage characteristics of the device is shown in Figure. 3. As the input frequency increases, the lobes in the first quadrant and third quadrant reduces to a linear form (Figure 3). Thus, at higher frequencies, the memristance property is lost and the device acts as a linear resistor. Figure 3 shows that $v(t)$ and $i(t)$ reach zero simultaneously, which is consistent with Eq. (1). To simulate memristor characteristics, we used OrCAD Capture and PSpice Lite 16.6. The memristance of a device ranges between positive values R_{ON} and R_{OFF} . If Chua's diode is substituted with a memristor, the nonlinear element should have a positive slope. Because the slope of the nonlinear element characterizes the stability of an equilibrium point at the point under consideration, the memristor with a positive slope produces a stable equilibrium point. At a stable equilibrium point, all waves settle onto the equilibrium point. Therefore, no oscillations are produced. Thus, the memristor cannot be directly substituted as the nonlinear element of the Chua's circuit. For the equilibrium point to be unstable, the slope of the nonlinear element should be negative. Adding a negative resistance in series does not yield a negative slope because of the current-voltage characteristics of a memristor in series with a negative resistor yield a positive slope. A negative slope occurs for a memristor in parallel with a negative resistor.

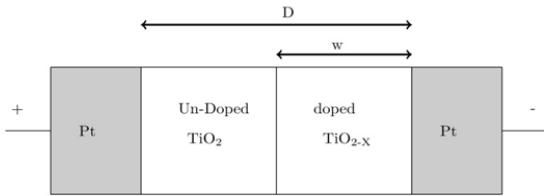


Figure 2: Memristor model of the two terminal Pt/TiO2/TiO2-X/Pt memristor

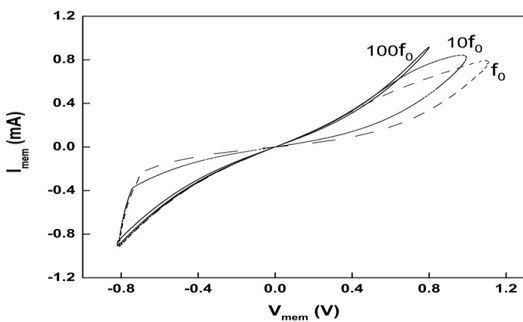


Figure 3: Hysteresis loop and Time waveforms of voltage and current: As the sweep frequency is increased ten times (10f0) and hundred times (100f0), there is a decrease in the lobes of the pinched hysteresis.

III. PROPOSED NEW MEMRISTOR BASED CHAOTIC CIRCUIT

Figure 4 shows chaotic circuit with the Chua's nonlinear device[7,8] substituted by a negative resistance and the memristor. The inductor L is in parallel with capacitor C2 to form a tank circuit that produces lossless oscillations. The nonlinear element locally passive, all trajectories would approach a stable equilibrium and the circuit would produce

no oscillations. However, because the nonlinear element is locally active, it supplies power to the external circuit. The resistor R interacts with capacitor C1 and the nonlinear element. Because R is a dissipative element, it restrains the growth of the trajectory, which forms a chaotic trajectory that is constrained to a particular region (basin of attraction).

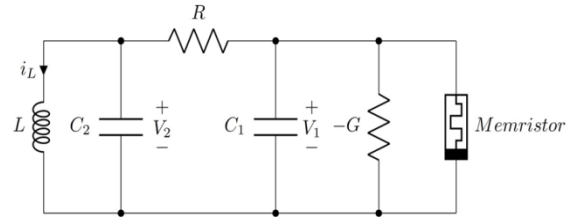


Figure 4: Proposed chaotic circuit. Chua's nonlinear element is exchanged by the parallel combination of the memristor and a negative resistance component.

Applying Kirchoff's law to proposed chaotic circuit, the set of differential equations are obtained (Equation 3).

$$\begin{aligned} \frac{dv_{C1}}{dt} &= \frac{1}{C_1} \left[\frac{v_{C2} - v_{C1}}{R} + \frac{v_{C1}}{G} - \frac{v_{C1}}{R_{mem}(w)} \right], \\ \frac{dv_{C2}}{dt} &= \frac{1}{C_2} \left[\frac{v_{C1} - v_{C2}}{R} + i_L \right], \\ \frac{di_L}{dt} &= -\frac{v_{C2}}{L}, \\ \frac{dw}{dt} &= \mu_V \frac{R_{ON}}{D} v_{C1}, \end{aligned} \quad (3)$$

where v_{C1} and v_{C2} are the voltages across the capacitor C1 and C2 and i_L is the current flowing through the inductor L, and $R_{mem}(w)$ is the memristance of the memristor. The evolution of w is controlled by the change in v_{C1} (3). The nonlinear device is thus a voltage-controlled memristor.

$$R_{mem}(w) = R_{ON} \frac{w(t)}{D} + R_{OFF} \left(1 - \frac{w(t)}{D} \right), \quad (4)$$

The parameter values for the HP memristor are $R_{ON} = 100 \Omega$, $R_{OFF} = 10 \text{ k}\Omega$, $\mu_V = 10\text{--}14 \text{ m}^2 \text{ s}^{-1} \text{ V}^{-1}$, $D = 10 \text{ nm}$. The circuit elements are $C_1 = 7.5 \text{ nF}$, $C_2 = 68 \text{ nF}$, $L = 18 \text{ mH}$, and $R = 1.8 \text{ k}\Omega$.

Numerical computational methods are utilized to determine the solutions of the physical system. The Runge-Kutta analysis method is applied to find the solution of the differential equations of the Chua's circuit [8] with a step size of $\Delta t = 0.001$. We used the ode45 function in MATLAB R2016a to solve the differential equation and analyze the solution. For the numerical analyses, the component values listed previously was applied. The resistance value R was varied to alter the behaviour of the proposed system.

IV. RESULTS OF THE PROPOSED NEW MEMRISTOR BASED CHAOTIC CIRCUIT

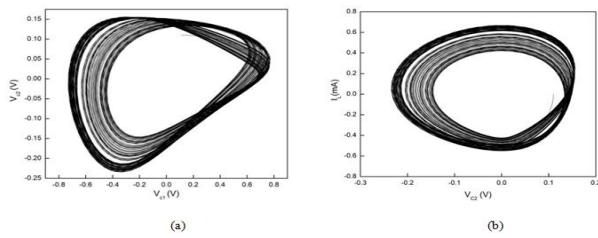


Figure 5: Chaotic trajectory and power spectrum: (a) Chaotic trajectory projection on plane; (b) Chaotic trajectory projection on plane

Figure 5 shows the chaotic trajectory and power spectrum of the circuit. The chaotic trajectory reveals a stable steady-state solution of Chua’s chaotic circuit and a homoclinic orbit: this is a strange attractor. The trajectory starts from an initial condition and then moves continually around the fixed point without ever intersecting its orbit. Although the system is based on a fourth-order differential equation, for any finite-time observations, the trajectory is irregular and does not return to its starting point. The power spectrum is characteristic of broadband noise, which indicates the existence of an aperiodic waveform. The power is not concentrated in a small number of frequencies but is distributed over a wide range of frequencies. For a periodic signal, for example, the power would not be distributed but concentrated in the dc component, the fundamental frequency component, and the harmonics.

V. CONCLUSION

This work presents a new chaotic circuit using memristor. In which the conventional Chua’s diode is replaced with a negative resistor which is in parallel with a memristor in the proposed chaotic circuit. The implemented memristor is based on the physical equations proposed by Hewlett-Packard. The memristor-based Chua’s circuit becomes chaotic with actual values and without requiring scaling. In addition, the memristor state equation is based on past and present values of w and the input voltage (or current), so Chua’s nonlinear element may be effortlessly replaced by a physical component of the memristor. Also, these type of chaotic behaviour system is useful in transmitting secure image and documents over wireless communication.

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