Harmonic Reduction Analysis for Electric Arc Furnace using Passive Filters and DSTATCOM

Priyashree S, Vidya H A

Abstract: The industrial furnaces are used globally for a wide range of applications from lab scale to complete production lines. They provide continuous high temperatures of 300°C to 3000°C with precise control of temperature and material flow to ensure consistent and uniform output. The furnace operators that deliver at higher efficacies are prone to power supply system challenges with greater potential for power quality implications on supply system. Hence such operators of high intensity power furnaces should consider design and development of appropriate mitigation techniques to upgrade the power supply quality. In this paper the mitigation of harmonics in Electric Arc Furnace is dealt with suitable passive filter and DSTATCOM techniques. Also, the performance analysis of Electric Arc Furnace has been carried out with MATLAB/SIMULINK based model using Sinusoidal pulse width modulation, hysteresis and the sliding mode controllers.

Index Terms: Electric Arc Furnace, Hysteresis controller, Passive filters, Sliding mode controller and Total harmonic reduction.

I. INTRODUCTION

Electric Arc Furnace (EAF) is one of the common loads in industry that introduce considerable distortion of current harmonics in distribution system. These unbalance load currents cause voltage fluctuations and voltage imbalance, affecting the operation of other electric equipments in the power system. Also there occurs initial decay of load current due to the existence of industrial furnaces in the distribution network. Hence appropriate actions are to be taken to reduce these power quality problems. Power quality analyzers (PQA) facilitate to identify power quality problems and assess the required compensation to reduce harmonics. Besides the energy storage systems and distributed generation, other devices used to solve PQ problems include dynamic voltage restorer, constant voltage transformer, isolation transformer, noise filters, static VAR compensators and harmonic filters (passive and active). With suitable interface or compensating devices, loads can be isolated from the disturbances that are originating from the grid. In the presence of passive filters the harmonic currents tend to flow along the impedance path. These filters are relatively inexpensive compared to the other mitigating devices. Usually low pass, high pass, single tuned, double tuned and C-type high-pass filters are the conventional passive filters implemented to decrease the harmonic content in line current.

In order to analyze the effective harmonic reduction, it necessitates use of common and precise model for EAF. In this work, a three-phase time domain EAF model is developed using MATLAB/SIMULINK.

II. MATHEMATICAL MODEL OF ELECTRIC ARC FURNACE

Power quality analysis was carried out at M/s JSW Steel Plant, Bellary with an EAF load. The online readings of voltage, current, power, power factor and other significant electrical parameters were recorded using PQA. Data gathered at the EAF is shown in Table I, which is obtained using PQA at the supply side of EAF installation.

The voltage and current waveform on the supply side of EAF is shown in Fig. 1. The current THD of EAF is observed to be 35.44% (as obtained using MATLAB program based Harmonic Analyzer for the data collected at the EAF steel plant) shown in Fig.2.

<table>
<thead>
<tr>
<th>Three Phase Supply</th>
<th>V₁ (Volts)</th>
<th>I₁ (Amps)</th>
<th>Real Power (KW)</th>
<th>Reactive Power (KVAR)</th>
<th>THD V (%)</th>
<th>THD I (%)</th>
<th>Power Factor</th>
</tr>
</thead>
<tbody>
<tr>
<td>R 1890 g</td>
<td>1362</td>
<td>0.0259</td>
<td>0.0226</td>
<td>1.46</td>
<td>9.31</td>
<td>0.7536</td>
<td></td>
</tr>
<tr>
<td>Y 1926 g</td>
<td>1070.25</td>
<td>0.0198</td>
<td>0.0191</td>
<td>2.34</td>
<td>12.18</td>
<td>0.7185</td>
<td></td>
</tr>
<tr>
<td>B 1830 g</td>
<td>1281.25</td>
<td>0.0276</td>
<td>0.0148</td>
<td>2.00</td>
<td>10.27</td>
<td>0.8811</td>
<td></td>
</tr>
</tbody>
</table>

Revised Manuscript Received on August 05, 2019.
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Mathematical model of EAF is developed based on the classical Cassie and Mayr models that are applied extensively for the study of phenomena of an electric arc in the high-voltage circuit breakers. The suggested model of EAF pronounces the arc furnace as a device consisting of two-terminal corresponding to variables arc current and voltage, as shown in Fig. 3. This model employs the analysis of node voltages and obtain solution such that it can be appropriately realized using MATLAB/SIMULINK.

Thus the general expression that indicates the conductance of EAF is represented by combining the Cassie and Mayr Models as given by Eq. (1).
From Eq. (1) the nine parameters: $g_{min}$, i, v, $I_o$, $P_o$, $E_o$, $\theta_1$, $\theta_0$ and $\alpha$ characterize the conductance of each phase for the proposed EAF model. The equivalent circuit of EAF using MATLAB is shown in Fig. 4. The subsystem of the selected model of EAF is shown in Fig 5.

The Supply Voltage and Current waveform on the supply side is shown in Fig. 6. The FFT analysis of EAF Cassie-Mayr model without filter is shown in Fig. 7. The supply current THD is obtained as 35.64%.

It is observed that the THD obtained from Cassie-Mayr model (35.64%) is approximately same as THD obtained at the EAF steel plant (35.44%). Hence, the harmonic reduction analysis of EAF is carried out using Cassie-Mayr model.

### III. HARMONIC REDUCTION ANALYSIS FOR EAF USING PASSIVE FILTER

#### A. About Passive Filters

The filters that are composed of R, L and C components are referred to as passive filters. They are simple filters without any active elements such as transistors or operational amplifiers. They are the representative of simplest application for a given system function as the number of necessary components are lesser.

Also, as they do not consist of active elements, supply of power is not mandatory. These passive filters operate better at large frequencies, since they do not possess bandwidth limitations such as that of operational amplifiers.

The passive filters are classified as series, shunt and their combination. Usually shunt passive filters are more preferable than series due to lower installation cost and better reactive power compensation capability.

#### B. MATLAB/SIMULINK Equivalent Model of EAF with Passive Filters

Analysis was carried out with the Passive filter based Cassie-Mayr model of EAF to observe reduction of harmonics in source current.
The simulations were carried out for different passive filters such as LC, High pass, Double tuned and C-Type High Pass. The Low Pass filter was connected on the supply side of the Cassie - Mayr model of EAF as shown in Fig. 8. The waveform of supply current with reduced harmonics for the Cassie-Mayr model of EAF with LC Filter is shown in Fig. 9. The FFT analysis of supply current for EAF with LC filter is as shown in Fig. 10. The THD of supply current was found to be reduced from 35.64% (without filter) to 5.58% (with LC filter).

The analysis of THD for supply current with High pass, Double tuned and C-Type High Pass filters are as tabulated in Table II. Hence, it is observed that C-Type High pass filters are suitable for EAF load for reduction of harmonics in supply current.

<table>
<thead>
<tr>
<th>Filter Type</th>
<th>Current THD (percentage)</th>
<th>% Reduction</th>
</tr>
</thead>
<tbody>
<tr>
<td>Without filter</td>
<td>35.64</td>
<td>--</td>
</tr>
<tr>
<td>LC</td>
<td>5.58</td>
<td>84.34</td>
</tr>
<tr>
<td>Double tuned</td>
<td>2.96</td>
<td>91.69</td>
</tr>
<tr>
<td>High pass</td>
<td>2.94</td>
<td>91.75</td>
</tr>
<tr>
<td>C type high pass</td>
<td>2.78</td>
<td>92.19</td>
</tr>
</tbody>
</table>

IV. HARMONIC REDUCTION ANALYSIS FOR EAF USING DSTATCOM

A. About DSTATCOM

DSTATCOM is a compensating device used to regulate the alternating current and provide better power quality for the distribution network. The system scheme of a typical DSTATCOM is shown in Fig. 11.

Fig. 8 Cassie-Mayr model of EAF with LC Filter
The closed loop path consists of current and voltage magnitude sensing block, power quality analyzer and DSTATCOM. In this scheme of DSTATCOM, the shunt current $I_{sh}$ is injected so as to correct the reduction of voltage by regulating the voltage across the $Z_{th}$ - impedance of the system. The value of shunt injected current depends on the output voltage of the converter. The current $I_{sh}$ is given by Eq (2) and (3).

$$I_{sh} = I_L - I_S = I_L - (V_{th} - V_L)Z_{th}$$ (2)

$$I_{sh} = I_L\delta - V_{th}Z_{th}\beta - \delta + V_LZ_{th}\gamma - \beta$$ (3)

The value of power injected $S_{sh}$ of the DSTATCOM is expressed in Eq (4).

$$S_{sh} = V_L I_{sh}$$ (4)

The impedance $Z_{th}$ of the load bus indicates the effectiveness of the DSTATCOM. The shunt current $I_{sh}$ is aligned in quadrature with load voltage $V_L$ such that the magnitude of voltage to be corrected can be attained irrespective of active power injection into the system. If the required amount of shunt current is lower, then with the lower apparent power injection same voltage correction can be attained for the system.

When the impedance of source is connected in parallel with impedance of the load, the DSTATCOM works such that the voltage of load bus is equal to the product of injected current and impedance of the device. The supply current harmonics is eliminated and provides reactive power compensation. It also aids to decrease the fluctuations of voltage at the pcc. Usually VSC with a suitable PWM offers quick and consistent control.
B. MATLAB/SIMULINK Equivalent Model of EAF with DSTATCOM

The system scheme of DSTATCOM shown in Fig. 11 is implemented for the proposed Cassie–Mayr model of EAF. In this work, the analysis of harmonic reduction for EAF with DSTATCOM is carried out for three types of controllers:

i. Sinusoidal Pulse Width Modulation (SPWM) control
ii. Hysteresis Current Controller (HCC) and
iii. Sliding Mode Controller (SMC)

The MATLAB/SIMULINK model of EAF with SPWM based DSTATCOM is as shown in Fig. 12. The supply current waveform of EAF with SPWM based DSTATCOM is as shown in Fig. 13. FFT Analysis at supply side of EAF is shown in Fig. 14. It is examined that the supply current THD decreases from 35.64% to 3.39%, with a net decrease of 90.48%.

Hence, it is observed that SMC based DSTATCOM provided better results for Electric arc furnace.

**TABLE: COMPARISON OF THD FOR DIFFERENT EAF CONTROL SCHEMES OF DSTATCOM**

<table>
<thead>
<tr>
<th>Connection scheme of EAF</th>
<th>Method of control</th>
<th>Current THD (%)</th>
<th>Reduction of THD (%)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Without Filters</td>
<td>--</td>
<td>35.64</td>
<td>--</td>
</tr>
<tr>
<td>With DSTATCOM (Cassie-Mayr Model)</td>
<td>SPWM</td>
<td>3.39</td>
<td>90.48</td>
</tr>
<tr>
<td></td>
<td>HCC</td>
<td>2.96</td>
<td>91.69</td>
</tr>
<tr>
<td></td>
<td>SMC</td>
<td>0.84</td>
<td>97.64</td>
</tr>
<tr>
<td>With DSTATCOM (Cassie-Mayr Model) (with Programmable Input)</td>
<td>SPWM</td>
<td>3.47</td>
<td>90.26</td>
</tr>
<tr>
<td></td>
<td>HCC</td>
<td>3.57</td>
<td>89.98</td>
</tr>
<tr>
<td></td>
<td>SMC</td>
<td>2.04</td>
<td>94.27</td>
</tr>
</tbody>
</table>

IV. CONCLUSION

Electric arc furnace introduce appreciable amount of current harmonics into the distribution system, which affects the performance of other equipments connected in the network. The behavior of Electric arc furnace was investigated using MATLAB/SIMULINK to mitigate their current harmonics. An equivalent model of Induction Furnace is developed and the Cassie-Mayr model of Electric arc furnace is proposed for the analysis of effective power quality improvement using passive filters and DSTATCOM.

The Simulation based passive filters such as LC, double tuned, high pass and C-type high pass filters were designed to reduce harmonics. The THD of current for C-Type High pass filter gave better harmonic reduction in supply current for the electric arc furnace model.

The effectiveness of DSTATCOM to reduce the current harmonics was investigated using MATLAB/SIMULINK for Electric arc furnace model. The DSTATCOM model executed with SPWM, HCC and SMC control scheme of DSTATCOM, where the SMC controller with programmable type input provided the better harmonic reduction than the other control schemes adapted for the proposed system.
REFERENCES:


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Priyashree S received her B.E. degree in Electrical and Electronics Engineering and M.E. in Power & Energy Systems from Bangalore University, Bangalore in 2001 and 2004 respectively. She has secured First Rank in M.E from Bangalore University. She has worked as Lecturer (2004 – 2008), Assistant Professor (2008 – 2012) and Associate Professor (2012 to till date) in Department of Electrical & Electronics Engineering, BNM institute of Technology, Bengaluru. She has completed her Ph.D (Electrical Sciences) in 2019, under Visvesvaraya Technological University, Belagavi, under the guidance of Dr. Vidya H A, Professor, Department of Electrical & Electronics Engineering, Global Academy of Technology, Bengaluru . Her research interests are in the areas of Power Quality and Signal processing.

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