

A Novel Power Factor Corrected Converter for Solar Pv Emulator



A.Santhi Mary Antony, D.Ramya, D.Godwin Immanuel,

Abstract: This Paper Presents A Pfc Converter Design For A Solar Pv Emulator. A Boost Converter Is Proposed As A Pfc Converter. The Photovoltaic Modules Are Connected In Series As An Array. The Pfc Buck-Boost Converter Circuit Provides The Compensated Voltage To The Pv Panel. The Pfc Converter Circuit Has Been Simulated In Simulink/Matlab. The Simulation Results Shows The Single Phase And Three Phase Output Voltages Along With The Pfc Correction.. The Pv Panel Compensates The Voltage Provided By The Power Source After Conversion From Ac To Dc.

Keywords: - Photovoltaic Module, Buck-Boost Converter, Pv Emulator, Pic Microcontroller

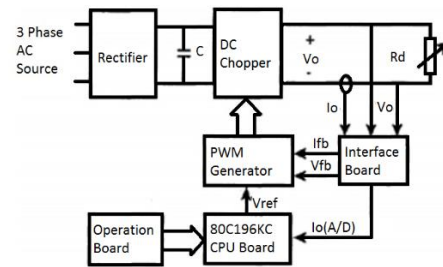


Fig 1:DC Chopper system circuit diagram

I. INTRODUCTION

Renewable energy is a vital area of development in recent years. Renewable sources are increasing rapidly. Solar system, Wind energy, fuel cell are the recent trends of development in the field of renewable energy [1]. Availability of solar is abundant, but when compared with wind energy, the wind sources are erratic in nature. Solar power generation is used in all the industries. Now days, industrialist generate their own power by erecting the solar panels in the industries [6]. The power required for usage of industry is generated and the remaining power is fed to the grid for the consumers. Each solar panel consists of an array of solar cells, which is fabricated by using aluminium material [2]. Solar power taken by the PV panel depends on the radiation of sunlight. But a minimum of 25degree Celsius is required for the PV cell to generate solar power. The solar power generated is DC power. The generated DC is fed to the DC panel through the diodes [5]. The generated power from DC is fed to the inverter for the conversion of DC to AC. The AC power is fed to the AC panel for the distribution to the industrial usage. The excess of AC power is fed to grid for the consumers.

II. DC CHOPPER BASED SYSTEM

The DC Chopper based PV emulator emulates the output characteristics of the PV modules [8]. The output characteristics of the simulator are similar to those of an actual PV module. The simulator has a very good dynamic response.

III. BUCK-BOOST CONVERTER BASED SYSTEM

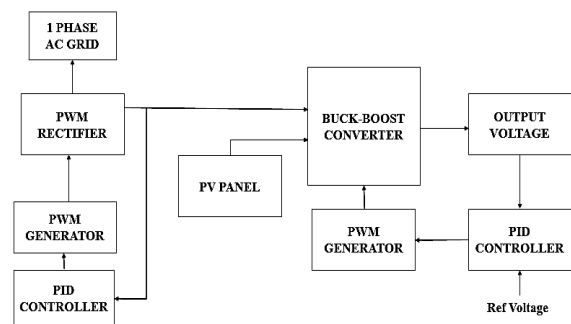


Fig 2: Buck-Boost Converter system block diagram

IV. MODES OF OPERATION OF BUCK BOOST CONVERTER

The Buck Boost converter has two modes of operation, when switch is ON and OFF.
Mode I: Switch is ON, Diode is OFF

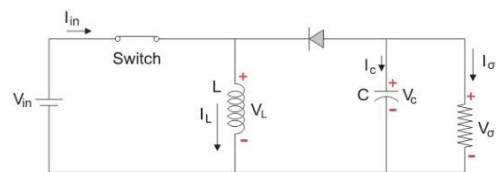


Fig 3: Buck-Boost converter switch on mode

When the switch is ON, the current flows in the closed circuit. The inductor stores its energy during the instant switch is ON. The PV emulator is connected across the load terminals [13]. At an instant of time period t_1 , the diode is in the reverse biased state. The capacitor is charged with upper plate positive. The power factor correction is done by switching the switches at different instant of time period.

Revised Manuscript Received on October 30, 2019.

* Correspondence Author

A.Santhi Mary Antony*, Department of Electrical and Electronics, Sathyabama Institute of Science and Technology, Chennai, Tamilnadu.

D.Ramya, Department of Electrical and Electronics, Sathyabama Institute of Science and Technology, Chennai, Tamilnadu.

D.Godwin Immanuel, Department of Electrical and Electronics, Sathyabama Institute of Science and Technology, Chennai, Tamilnadu.

© The Authors. Published by Blue Eyes Intelligence Engineering and Sciences Publication (BEIESP). This is an open access article under the CC BY-NC-ND license (<http://creativecommons.org/licenses/by-nc-nd/4.0/>)

A Novel Power Factor Corrected Converter For Solar Pv Emulator

Mode II: Switch is OFF, Diode is ON

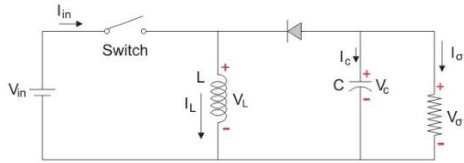


Fig 4: Buck-Boost converter switch off mode

When the switch is turned OFF, the polarity of terminals across the inductor is interchanged. The diode is forwards biased [14]. The capacitor is charged with upper plate positive and lower plate negative. The current freewheels through the closed circuit.

$$(\Delta i_L)_{open} = (V_o/L)(1-D)T$$

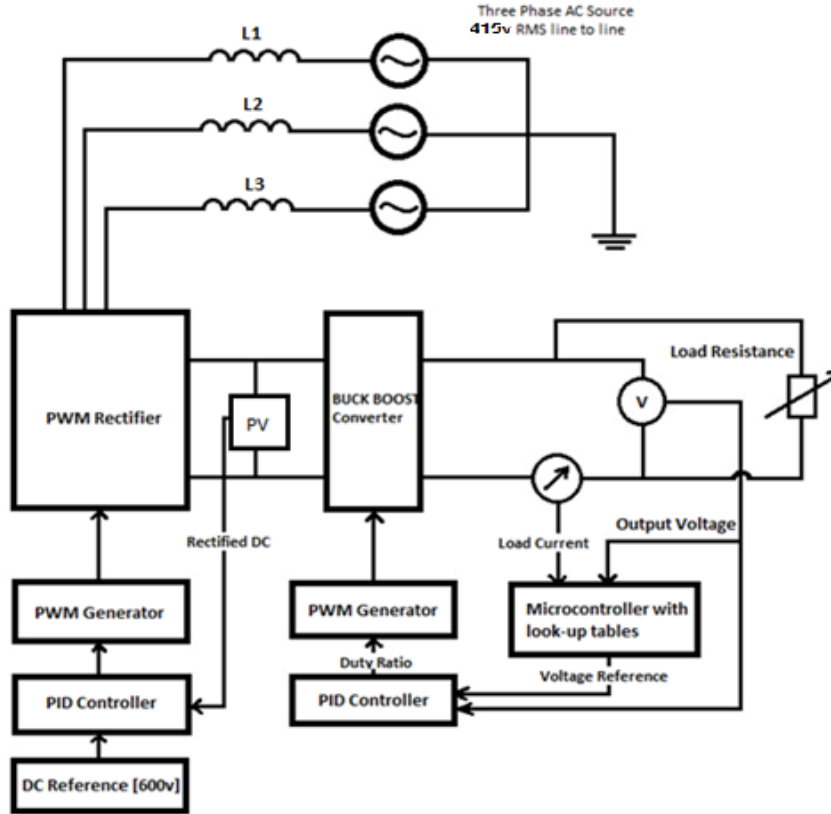


Fig 5: Buck-boost Converter Circuit diagram

V. SIMULATION RESULTS

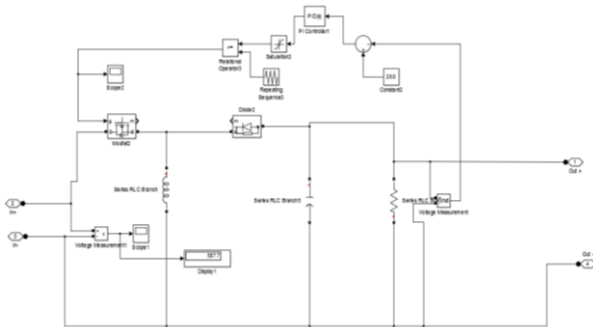


Fig 6: Closed loop Buck-Boost converter

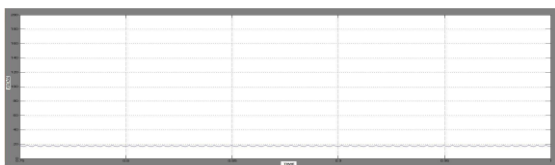


Fig 7: Output voltage for the single phase model made for hardware

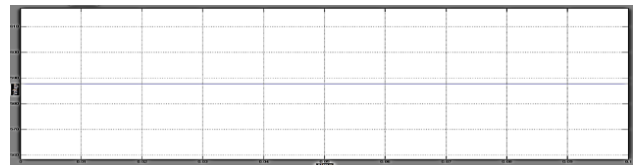


Fig 8: PV Panel output of 585 volts

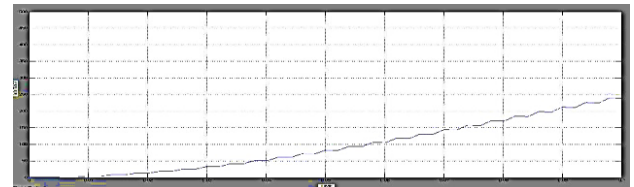


Fig 9: Output for buck boost converter at 238.9 volts

VI. THREE PHASE SIMULATION

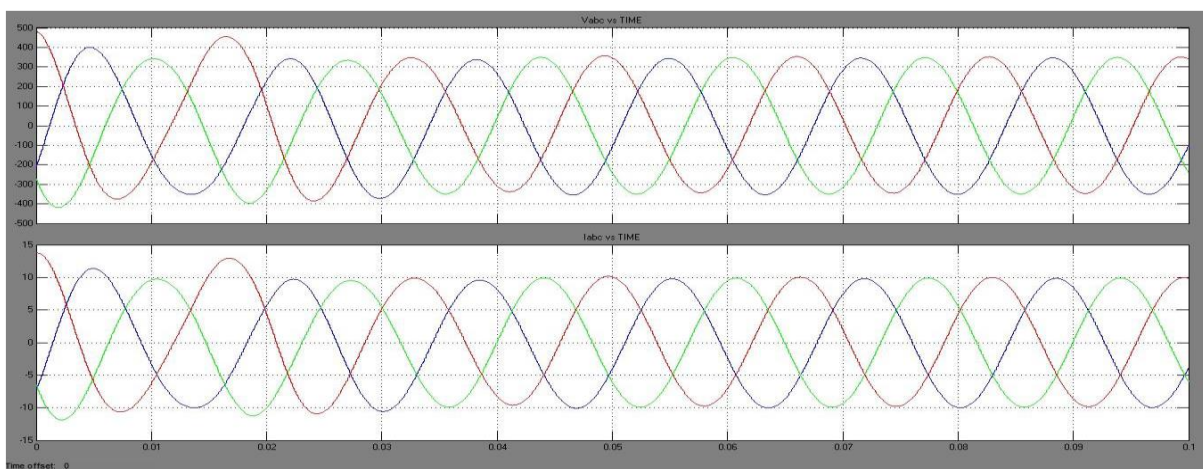
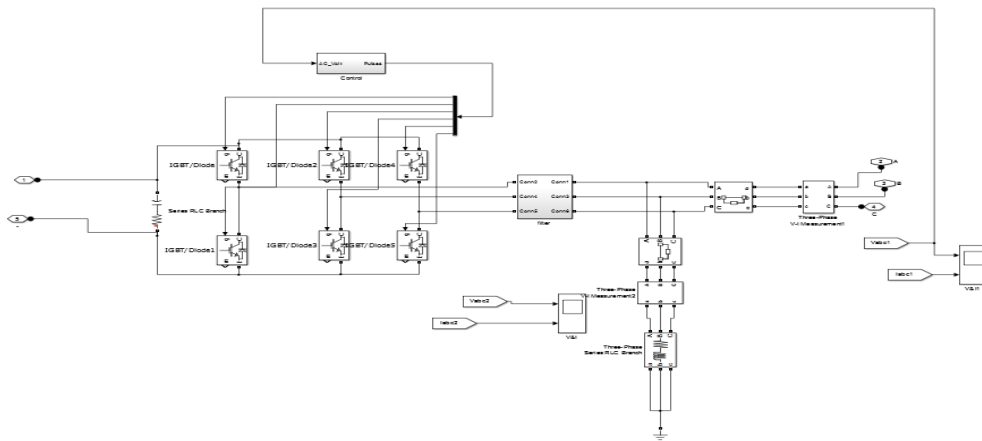


Fig 10: Three phase simulation

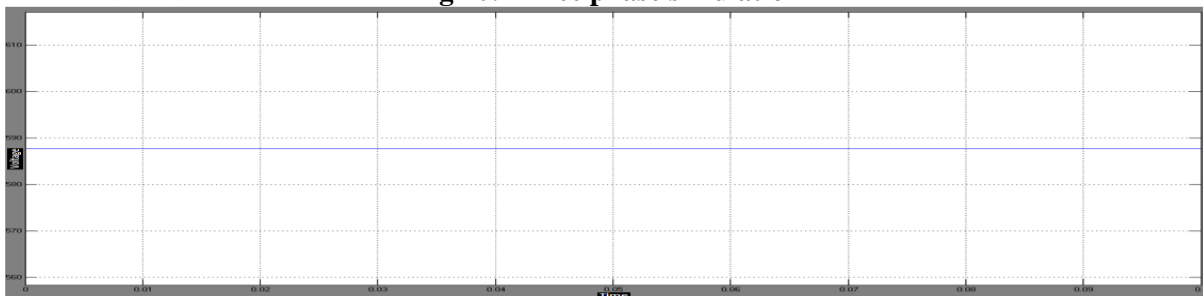


Fig 11: PV panel output for three phase model

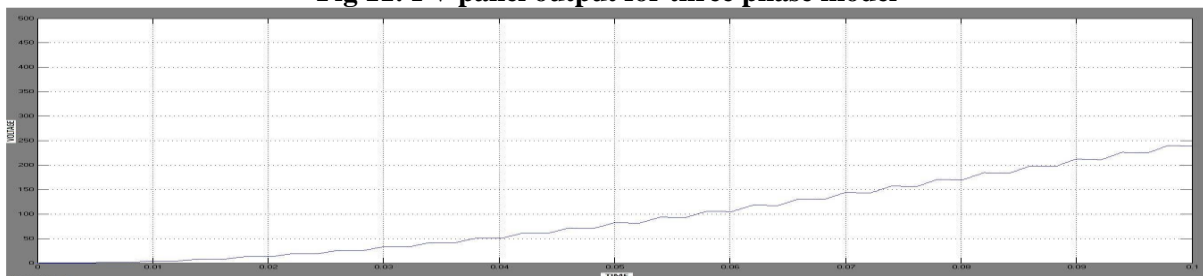


Fig 12: Load output for three phase

VII. HARDWARE OPERATION

The transformer steps down the voltage from 230 V to 12V. The 12v supply is given to bridge rectifier made using IN4007 diodes and the converted from pulsating DC to actual DC current. IC7805 voltage regulator reduces the voltage to 5v for PIC microcontroller to be operable. A PIC microcontroller of PIC 16F series is used which provides the reference voltage to the driver circuit operating the

MOSFETs present in the converter circuit. The microcontroller uses a crystal oscillator to provide the voltage signal and grounding is handled by 10pF 1V ceramic capacitors. The Driver circuit amplifies the voltage using opto-coupler and provides pulses to the MOSFETs. The rectified voltage from the transformer as DC voltage is given as an input to the converter circuit along with the PV panel compensation.

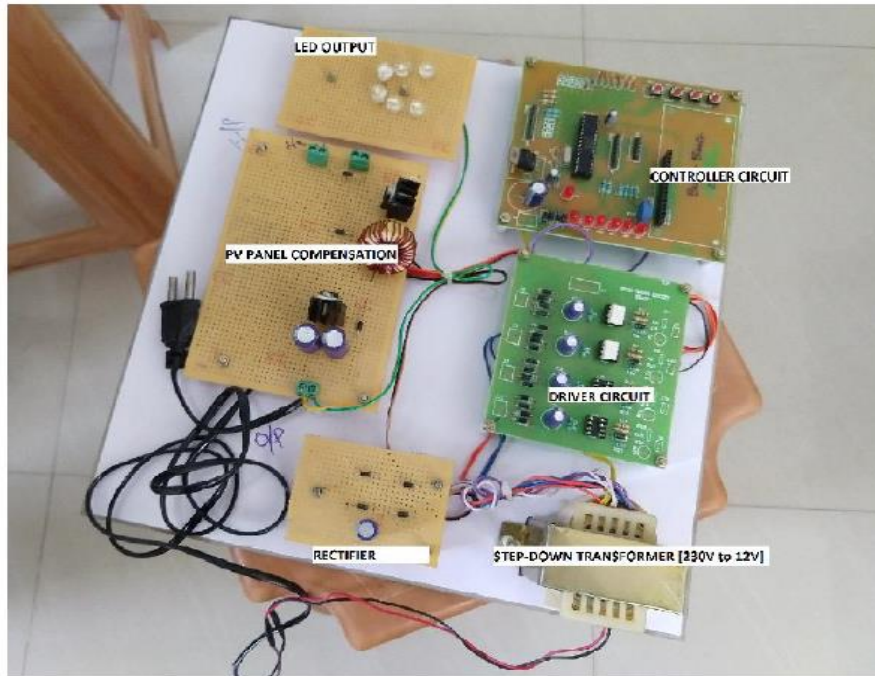


Fig 13: Hardware circuit
COMPARISON TABLE

DC-DC Chopper	Buck-Boost
Output voltage is about 44v	Output voltage is about 238.5v
Higher voltages cannot be reached easily.	Higher voltages can be reached
Spike in voltage is lower(about 5v)	Spike in voltage is higher(about 15v)
Steady State Error is higher(about 4v for 44v)	Steady State Error is reduced(about 1.5v for 238.5v)
Power Factor Efficiency is lower(about .82)	Power Factor Efficiency is higher(about .9)
Operation in variable Temperatures can lead to losses (about 20v variation)	Operation can be done at variable temperatures (about 10v variation)

VIII. CONCLUSION

In this paper, a power electronic circuit based photovoltaic (PV) simulator with power factor correction converter is proposed. An array of photovoltaic modules connected in series. A power factor corrected buck-boost converter circuit is presented which is able to reduce the temperature of the PV panel at different condition. The designed circuit has been modelled and simulated in Simulink MATLAB environment. The simulation results has been compared with the designed hardware. Thus, the proposed power factor corrected converter can be used in laboratory environments for research purposes.

REFERENCES

1. KelamBhargav, Santhi Mary Antony A, "MPPT controller based solar power generating using a multilevel inverter", International Journal of Engineering and Technology (IJET) 2106,
2. K Babul Reddy, Santhi Mary Antony. A, "A single sensor based PFC Sepic converter fed BLDC motor drive for fan application", International Journal of Applied Engineering research"2015
3. A.Santhi Mary Antony ,Dr.Godwin Immanuel, "A Novel Single Phase bridgeless AC/DC PFC converter for Low Total Harmonics Distortion and High Power Factor", International Journal of Power Electronics and Drive System (IJPEDS) , March 2018.
4. Santhi Mary Antony.A, "Closed loop control of three port converter with high voltage gain", International Journal of Engineering and Technology (IJET) 2015, Vol 7, No.4, pp. 1224-1235. ISSN No. 0975-4024.
5. Santhi Mary Antony.A, "Performance Comparison of LLC Resonant Based Multioutput Converter and Single Inductor Boost Based Multi-output Converter For LED Driver Applications" International Journal of Engineering and Technology (IJET) 2016, Vol 8, No.5, pp. 1899-1909. ISSN No. 0975-4024.
6. A. Santhi Mary Antony, D. Ramya and D. Godwin Immanuel , "Energy Balance Cascaded Multilevel Inverter For Photovoltaic Application", ARPN Journal of Engineering and Applied Sciences (2017), Vol. 12, No. 23, pp. 6989 – 6993. ISSN No. 1819-6608.
7. A.Santhi MaryAntony , Dr.Godwin Immanuel, "Bridgeless Isolated Cuk-Converter with Bumpless Control for Reduced THD and Power Factor Correction", Journal of Advanced Research in Dynamical & Control Systems (2018), Vol. 10, 09-Special Issue, pp. 1357 – 1363.
8. Santhi Mary Antony.A, "Cascaded multilevel inverter of 11 levels for RL load with reduced distortion", Indian Journal of Science and Technology (INDJST), Vol 08, No. 19 (2015). ISSN No. 0974-5645.
9. J. Sangeetha, A. Santhi Mary Antony, Ramya D, "A boost multi-output flyback converter for electric vehicle applications", Research Journal Of Applied sciences, Engineering and Technology, Vol 10, No. 10 (2015), pp. 1133-1140. ISSN No. 2040-7467.
10. Ramya D, J. Sangeetha, A.Santhi Mary Antony," A Soft Switched Safety Enhanced Single Output Flyback Converter", Research Journal Of Applied sciences, Engineering and Technology, Vol 10, No. 6 (2015), pp. 688-693. ISSN No. 2040-7467.
11. Ramya D, A.Santhi Mary Antony, Dr.Godwin Immanuel, Dr.G.Nagarajan, "Design of interleaved flyback converter", International Journal of Intelligent Enterprise, Vol. 6, No. 1, 2019, pp 59 -76.
12. Ramya D, A.Santhi Mary Antony, Dr.Godwin Immanuel, "Development of high precision with suitable hysteresis for automatic voltage regulator", International journal of Innovative technology and exploring engineering, ISSN: 2278-3075, Volume-8 Issue-8 June, 2019, pp 801 – 804.
13. Ramya D, A.Santhi Mary Antony ," A reconfigurable five/seven level inverter with reduced switching losses", 6th International Conference on Computation of Power, Energy, Information and Communication, ICCPEIC 2017, DOI: [10.1109/ICCPEIC.2017.8290442](https://doi.org/10.1109/ICCPEIC.2017.8290442), INSPEC Accession Number: 17580331
14. Santhi Mary Antony.A, "Non Linear First Order ADRC For Speed Control of Induction Motor", International Journal of Pharmacy and Technology" (2016), Vol 8, No.3, pp. 16569-16574. ISSN No. 0975-766X.
15. D. Godwin Immanuel, G. Selva kumar, and ,C. Christober Asir Rajan, "A Multi Objective Hybrid
16. Differential Evolution Algorithm assisted Genetic Algorithm Approach for Optimal Reactive Power
17. and Voltage Control", International Journal of Engineering and Technology, Vol.6, Issue-1,
18. 2014,pp.199-203.