

UPQC-S: A Novel Concept of Simultaneous Voltage Sag/Swell and Load Reactive Power Compensations Utilizing Series Inverter of UPQC

K.Saranya Reddy, A.Bhaktavastala, V.Raveendra Reddy

Abstract This paper introduces a new concept of optimal utilization of a unified power quality conditioner (UPQC). The series inverter of UPQC is controlled to perform simultaneous 1) voltage sag/swell compensation and 2) load reactive power sharing with the shunt inverter. The active power control approach is used to compensate voltage sag/swell and is integrated with theory of power angle control (PAC) of UPQC to coordinate the load reactive power between the two inverters. Since the series inverter simultaneously delivers active and reactive powers, this concept is named as UPQC-S (S for complex power). A detailed mathematical analysis, to extend the PAC approach for UPQC-S, is presented in this paper. MATLAB/SIMULINK-based simulation results are discussed to support the developed concept. Finally, the proposed concept is validated with a digital signal processor-based experimental study.

Key Words: UPQC, UPQC-S, UPFC, PAC

I. INTRODUCTION

The modern power distribution system is becoming highly vulnerable to the different power quality problems. The extensive use of nonlinear loads is further contributing to increased current and voltage harmonics issues. Furthermore, the penetration level of small/large-scale renewable energy systems based on wind energy, solar energy, fuel cell, etc., installed at distribution as well as transmission levels is increasing significantly. This integration of renewable energy sources in a power system is further imposing new challenges to the electrical power industry to accommodate these newly emerging distributed generation systems. To maintain the controlled power quality regulations, some kind of compensation at all the power levels is becoming a common practice. At the distribution level, UPQC is a most attractive solution to compensate several major power quality problems. The general block diagram representation of a UPQC-based system is shown in Fig. 1. It basically consists of two voltage source inverters connected back to back using a common dc bus capacitor. This paper deals with a novel concept of optimal utilization of a UPQC.

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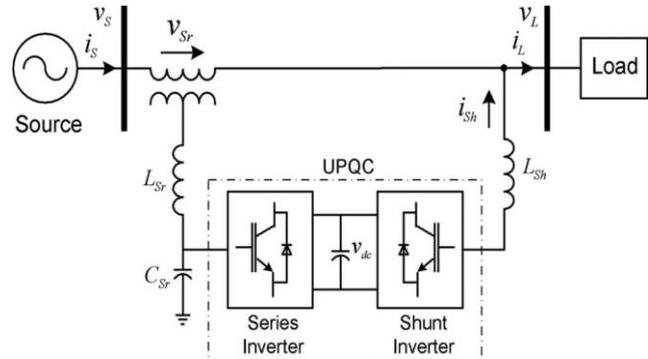


Fig.1. Unified power quality conditioner (UPQC) system configuration.

The voltage sag/swell on the system is one of the most important power quality problems. The voltage sag/swell can be effectively compensated using a dynamic voltage restorer, series active filter, UPQC, etc.. Among the available power quality enhancement devices, the UPQC has better sag/swell compensation capability. Three significant control approaches for UPQC can be found to control the sag on the system: 1) active power control approach in which an in-phase voltage is injected through series inverter, popularly known as UPQC-P; 2) reactive power control approach in which a quadrature voltage is injected [23], [24], known as UPQC-Q; and 3) a minimum VA loading approach in which a series voltage is injected at a certain angle, in this paper called as UPQC-V_{Amin}. In a minimum VA loading approach, the series inverter voltage is injected at an optimal angle with respect to the source current. Besides the series inverter injection, the current drawn by the shunt inverter, to maintain the dc link voltage and the overall power balance in the network, plays an important role in determining the overall UPQC VA loading. The reported paper on UPQC-V_{Amin} is concentrated on the optimal VA load of the series inverter of UPQC especially during voltage sag condition. The PAC concept suggests that with proper control of series inverter voltage the series inverter successfully supports part of the load reactive power demand, and thus reduces the required VA rating of the shunt inverter. The PAC of UPQC concept determines the series injection angle by estimating the power angle δ . Similar to PAC of UPQC, the reactive power flow control utilizing shunt and series inverters is also done in a unified power flow controller (UPFC). A UPFC is utilized in a power transmission system whereas a UPQC is employed in a power distribution system to perform the shunt and series compensation simultaneously.



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The primary objective of a UPFC is to control the flow of power at fundamental frequency. In this paper, the concept of PAC of UPQC is further expanded for voltage sag and swells conditions. This modified approach is utilized to compensate voltage sag/swell while sharing the load reactive power between two inverters. Since the series inverter of UPQC in this case delivers both active and reactive powers, it is given the name UPQCS (S for complex power).

A. Proposed Project

- 1) The series inverter of UPQC-S is utilized for simultaneous voltage sag/swell compensation and load reactive power compensation in coordination with shunt inverter.
- 2) In UPQC-S, the available VA loading is utilized to its maximum capacity during all the working conditions contrary to UPQC-VAm in where prime focus is to minimize the VA loading of UPQC during voltage sag condition.
- 3) The concept of UPQC-S covers voltage sag as well as swell scenario.

In this paper, a detailed mathematical formulation of PAC for UPQC-S is carried out. The feasibility and effectiveness of the proposed UPQC-S approach are validated by simulation as well as experimental results.

II. U.P.F.C

The UPFC is a combination of a static compensator and static series compensation. It acts as a shunt compensating and a phase shifting device simultaneously.

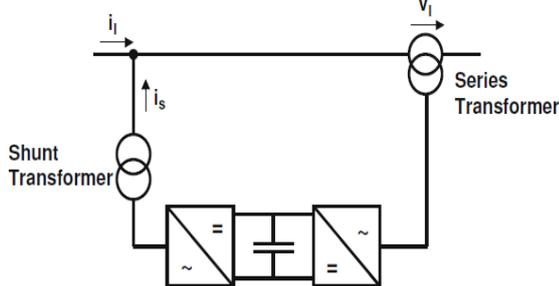


Fig.2.Principle configuration of an UPFC

The UPFC consists of a shunt and a series transformer, which are connected via two voltage source converters with a common DC-capacitor. The DC-circuit allows the active power exchange between shunt and series transformer to control the phase shift of the series voltage. The series converter needs to be protected with a Thyristor bridge.

III.FUNDAMENTALS OF PAC CONCEPT

The concept of PAC of UPQC suggests that with proper control of the power angle between the source and load voltages, the load reactive power demand can be shared by both shunt and series inverters without affecting the overall UPQC rating.

The phasor representation of the PAC approach under a rated steady-state condition is shown in Fig.3. According to

this theory, a vector \vec{V}_{Sr} with proper magnitude V_{Sr} and phase angle ϕ_{Sr} when injected through series inverter gives a power angle δ boost between the source V_s and resultant load V'_L voltages maintaining the same voltage magnitudes. This

power angle shift causes a relative phase advancement between the supply voltage and resultant load current I'_L , denoted as angle β .

For a rated steady-state condition

$$|V_s| = |V_L| = |V'_L| = |V''_L| = k \quad (1)$$

Using Fig. 3, phasor \vec{V}_{Sr} can be defined as

$$\begin{aligned} \vec{V}_{Sr} &= |V_{Sr}| \angle \phi_{Sr} \\ &= (k \cdot \sqrt{2} \cdot \sqrt{1 - \cos \delta}) \angle \left\{ 180^\circ - \tan^{-1} \left(\frac{\sin \delta}{1 - \cos \delta} \right) \right\} \\ &= \left(k \cdot \sqrt{2} \cdot \sqrt{1 - \cos \delta} \right) \angle \left(\frac{90^\circ + \delta}{2} \right) \end{aligned} \quad (2)$$

Where

$$\delta = \sin^{-1} \left(\frac{Q_{Sr}}{P_L} \right) \quad (3)$$

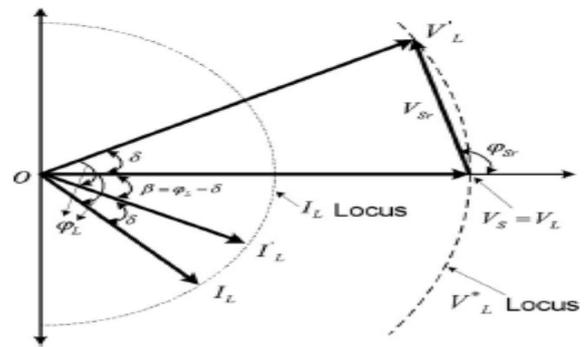


Fig.3. Concept of PAC of UPQC

A. Voltage SAG/SWELL Compensation Utilizing UPQC-P and UPQC-Q

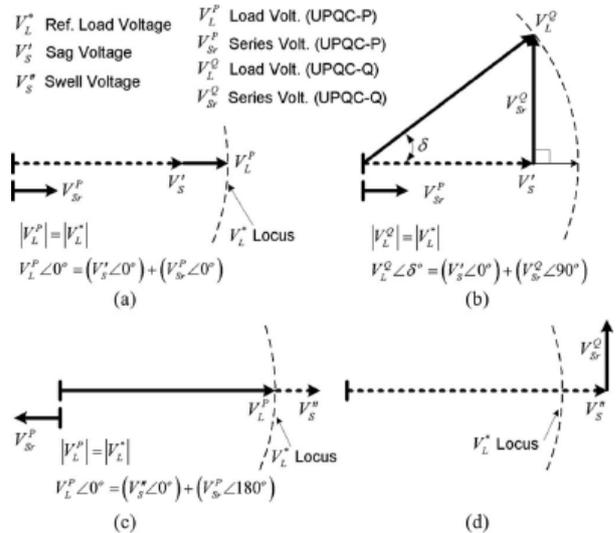


Fig. 4 Voltage sag and swell compensation using UPQC-P and UPQC-Q: phasor representation. (a) Voltage Sag (UPQC-P). (b) Voltage Sag (UPQC-Q). (c) Voltage Swell (UPQC-P). (d) Voltage Swell (UPQC-Q).



The voltage sag on a system can be compensated through active power control and reactive power control methods. Fig.4 shows the phasor representations for voltage sag compensation using active power control as in UPQC-P [see Fig. 4(a)] and reactive power control as in UPQC-Q [see Fig. 4(b)]. Fig. 4(c) and (d) shows the compensation capability of UPQC-P and UPQC-Q to compensate a swell on the system. For a voltage swell compensation using UPQC-Q [see Fig. d(d)], the quadrature component injected by series inverter does not intersect with the rated voltage locus. Thus, the UPQC-Q approach is limited to compensate the sag on the system.

B. PAC Approach Under Voltage SAG Condition

Consider that the UPQC system is already working under PAC approach, i.e., both the inverters are compensating the load reactive power and the injected series voltage gives a power angle δ between resultant load and the actual source voltages. If a sag/swell condition occurs on the system, both the inverters should keep supplying the load reactive power, as they were before the sag.

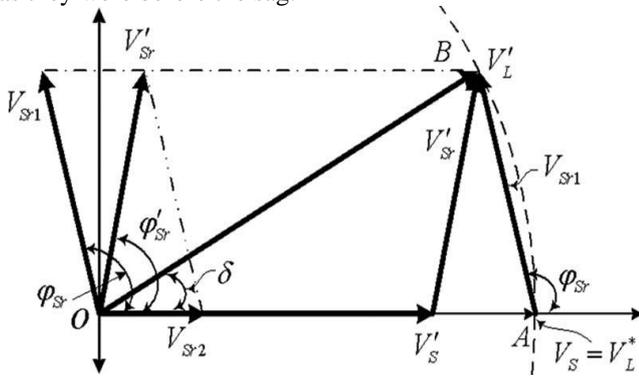


Fig.5 Phasor representation of the proposed UPQC-S approach under voltage sag condition.

Let us represent a vector \vec{V}_{Sr1} responsible to compensate the load reactive power utilizing PAC concept and vector \vec{V}_{Sr2} responsible to compensate the sag on the system using active power control approach. Thus, for simultaneous compensation, as noticed from Fig.5, the series inverter should now supply a component which would be the vector sum of \vec{V}_{Sr1} and \vec{V}_{Sr2} . This resultant series inverter voltage \vec{V}'_{Sr} will maintain the load voltage magnitude at a desired level such that the drop in source voltage will not appear across the load terminal.

For load reactive power compensation using PAC concept

$$\vec{V}_{Sr1} = \vec{V}'_L - \vec{V}_S \tag{4}$$

$$V_{Sr1} \angle \phi_{Sr} = V'_L \angle \delta - V_S \angle 0^\circ \tag{5}$$

For voltage sag compensation using active power control approach

$$\vec{V}_{Sr2} = \vec{V}^*_L - \vec{V}'_S \tag{6}$$

$$V_{Sr2} \angle 0^\circ = V^*_L \angle 0 - V'_S \angle 0^\circ \tag{7}$$

For simultaneous load reactive power and sag compensation

$$V'_{Sr} \angle \phi'_{Sr} = V_{Sr1} \angle \phi_{Sr} + V_{Sr2} \angle 0 \tag{8}$$

(i) Series Inverter Parameter Estimation under Voltage Sag

In this section, the required series inverter parameters to achieve simultaneous load reactive power and voltage sag compensations are computed. Fig. 6 shows the detailed phasor diagram to determine the magnitude and phase of series injection voltage.

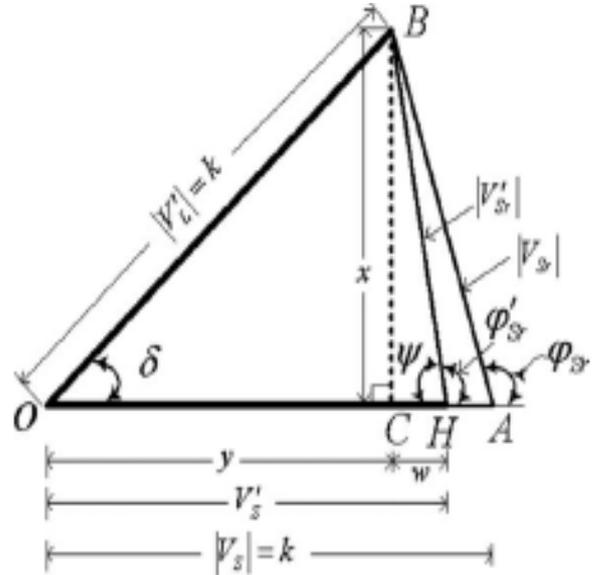


Fig. 6 Detailed phasor diagram to estimate the series inverter parameters for the proposed UPQC-S approach under voltage sag condition.

The voltage fluctuation factor k_f which is defined as the ratio of the difference of instantaneous supply voltage and rated load voltage magnitude to the rated load voltage magnitude is represented as

$$k_f = \frac{V_S - V'_L}{V'_L} \tag{9}$$

Representing (9) for sag condition under PAC

$$k_f = \frac{V'_S - V'_L}{V'_L} = \frac{V'_S - k}{k} \tag{10}$$

Let us define

$$1 + k_f = n_0 \tag{11}$$

To compute the magnitude of \vec{V}'_{Sr} , from $\triangle CHB$ in Fig. 6

$$w = l(CH) = n_0 \cdot k - y \tag{12}$$

$$|V'_{Sr}| = k \cdot \sqrt{1 + n_0^2 - 2 \cdot n_0 \cdot \cos \delta} \tag{13}$$

To compute the phase of \vec{V}'_{Sr}

$$\angle CHB = \angle \psi = \tan^{-1} \left(\frac{x}{w} \right) = \tan^{-1} \left(\frac{\sin \delta}{n_0 - \cos \delta} \right) \tag{14}$$

Therefore,

$$\angle \phi'_{Sr} = 180^\circ - \angle \psi \tag{15}$$

Equations (13) and (15) give the required magnitude and phase of series inverter voltage of UPQC-S.

(ii) Shunt Inverter Parameter Estimation Under Voltage Sag

The phasor diagram based on different currents is represented in Fig. 7.

The current I_{Sh} represents the required current if the shunt inverter is used alone to compensate the total load reactive power demand.

Thus, to support the series inverter to inject the required voltage for load reactive power and sag compensations, the shunt inverter should now deliver the current I_{S_h} . Fig. 8 represents the phasor diagram to compute the shunt inverter injected current magnitude and its phase angle.

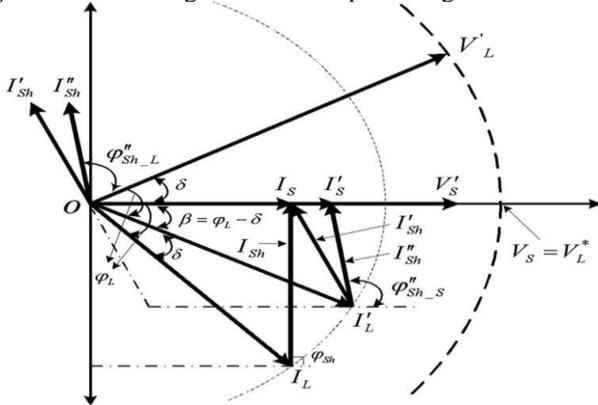


Fig. 7. Current-based phasor representation of the proposed UPQC-S approach under voltage sag condition.

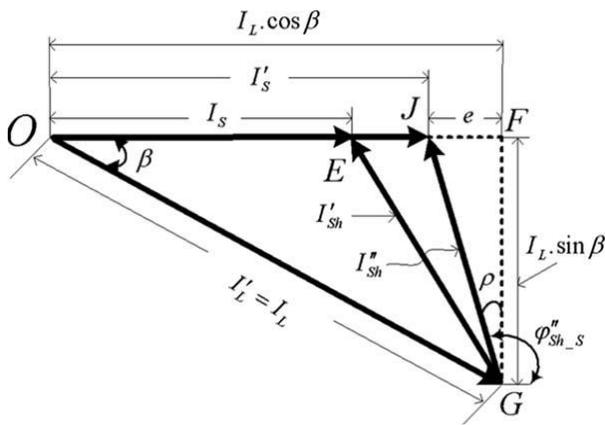


Fig. 8. Detailed phasor diagram to estimate the shunt inverter parameters

To support the active power required during voltage sag condition, the source delivers the extra source current.

During voltage sag

$$I'_S = \frac{I_L}{1 + k_f} \cdot \cos \varphi_L \quad (16)$$

$$\frac{1}{1 + k_f} = k_O \quad (17)$$

Let

$$I'_S = k_O \cdot I_L \cdot \cos \varphi_L \quad (18)$$

In ΔGFJ (see Fig. 5.6)

$$I''_{Sh} = \sqrt{(I'_L \cdot \sin \beta)^2 + [I'_L \cdot (\cos \beta - k_O \cdot \cos \varphi_L)]^2} \quad (19)$$

$$I''_{Sh} = I'_L \cdot \sqrt{1 + k_O^2 \cdot \cos^2 \varphi_L - 2 \cdot k_O \cdot \cos \beta \cdot \cos \varphi_L} \quad (20)$$

$$\rho = \tan^{-1} \left(\frac{\cos \beta - k_O \cdot \cos \varphi_L}{\sin \beta} \right) \quad (21)$$

$$\angle \varphi''_{Sh-S} = \angle \rho + 90^\circ \quad (22)$$

$$\angle \varphi''_{Sh-L} = (\angle \rho + 90^\circ) - \delta \quad (23)$$

Equations (20) and (23) give the required magnitude and phase angle of a shunt inverter .

C. PAC Approach Under Voltage SWELL Condition

The phasor representation for PAC of UPQC-S during a voltage swell on the system is shown in Fig. 9. Let us represent a vector V_{Sr3} responsible to compensate the swell on the system using active power control approach. For simultaneous compensation, the series inverter should supply the V_{Sr1} component to support the load reactive power and V_{Sr3} to compensate the swell on the system. The resultant series injected voltage V''_{S1} would maintain the load voltage magnitude at a desired level while supporting the load reactive power.

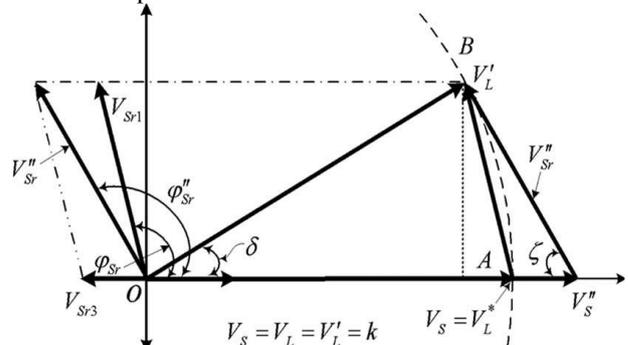


Fig 9. Phasor representation of the proposed UPQC-S approach under voltage Swell condition.

For voltage swell compensation using active power control approach

$$V_{Sr3} = V_L - V''_{S1} \quad (24)$$

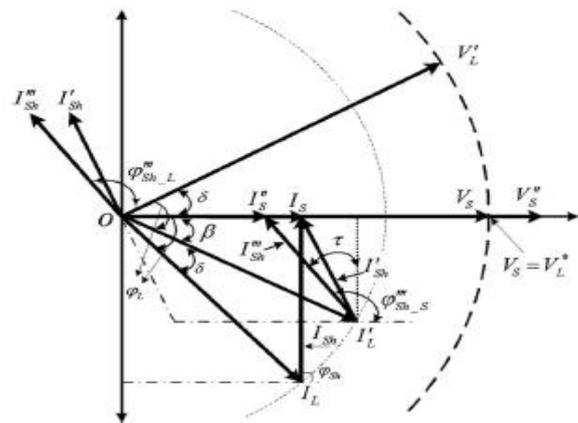


Fig. 10. Current-based phasor representation of the proposed UPQC-S approach Under voltage swell condition.



$$V_{Sr3} \angle 180^\circ = V_L^* \angle 0^\circ - V_S'' \angle 180^\circ. \quad (25)$$

For simultaneous load reactive power and voltage swell compensations

$$\vec{V}_{Sr}'' = \vec{V}_{Sr1} + \vec{V}_{Sr3} \quad (26)$$

For series inverter

$$|V_{Sr}''| = k \cdot \sqrt{1 + n_o^2 - 2 \cdot n_o \cdot \cos \delta} \quad (27)$$

$$\angle CLB = \angle \zeta = \tan^{-1} \left(\frac{\sin \delta}{n_o - \cos \delta} \right) \quad (28)$$

$$\angle \varphi_{Sr}'' = 180^\circ - \angle \zeta. \quad (29)$$

Fig.10 shows the phasor representation for different currents under PAC of UPQC-S under a voltage swell condition.

This reduced shunt inverter current is represented as I_{Sh}'' .

For shunt inverter (see Fig. 5.8)

$$I_{Sh}'' = I_L' \cdot \sqrt{1 + k_o^2 \cdot \cos^2 \varphi_L - 2 \cdot k_o \cdot \cos \beta \cdot \cos \varphi_L} \quad (30)$$

$$\tau = \tan^{-1} \left(\frac{\cos \beta - k_o \cdot \cos \varphi_L}{\sin \beta} \right) \quad (31)$$

$$\angle \varphi_{Sh-L}''' = (\angle \tau + 90^\circ) - \delta. \quad (32)$$

It can be noted that the equations for voltage sag and swell compensation utilizing the PAC of UPQC-S are identical.

D. ACTIVE-REACTIVE POWER FLOW THROUGH UPQC-S

The per-phase active and reactive powers flow through the UPQC-S during the voltage sag/swell is determined in this section.

(i) Series Inverter of UPQC-S

For active power

$$P_{Sr}' = V_{Sr}' \cdot I_S' \cdot \cos \varphi_{Sr}'. \quad (33)$$

From Fig. 6

$$P_{Sr}' = V_{Sr}' \cdot I_S' \cdot \cos(180^\circ - \psi) \quad (34)$$

$$P_{Sr}' = V_{Sr}' \cdot I_S' \cdot (-\cos \psi) \quad (35)$$

$$P_{Sr}' = -V_{Sr}' \cdot I_S' \cdot \left(\frac{\omega}{V_{Sr}'} \right) \quad (36)$$

$$P_{Sr}' = -I_S' \cdot k \cdot (n_o - \cos \delta). \quad (37)$$

The increase I_S' or decrease I_{Sin}'' in the source current magnitudes during the voltage sag or swell condition, respectively, is represented as

$$I_S' = I_S'' = k_o \cdot I_L \cdot \cos \varphi_L. \quad (38)$$

Therefore,

$$P_{Sr,PAC} = P_{Sr}' = -k_o \cdot (n_o - \cos \delta) \cdot (P_L) \quad (39)$$

For reactive power

$$Q_{Sr}' = V_{Sr}' \cdot I_S' \cdot \sin \varphi_{Sr}'. \quad (40)$$

From Fig. 6

$$Q_{Sr}' = V_{Sr}' \cdot I_S' \cdot \sin(180^\circ - \psi) \quad (41)$$

$$Q_{Sr}' = V_{Sr}' \cdot I_S' \cdot \sin \psi \quad (42)$$

$$Q_{Sr}' = V_{Sr}' \cdot I_S' \cdot \left(\frac{x}{V_{Sr}'} \right). \quad (43)$$

Therefore,

$$Q_{Sr,PAC} = Q_{Sr}' = k_o \cdot (\sin \delta) \cdot (P_L). \quad (44)$$

Using (39) and (44), the active and reactive power flow through series inverter of UPQC-S during voltage sag/swell condition can be calculated.

(ii) Shunt Inverter of UPQC-S

The active and reactive power handled by the shunt inverter

as seen from the source side is determined as follows

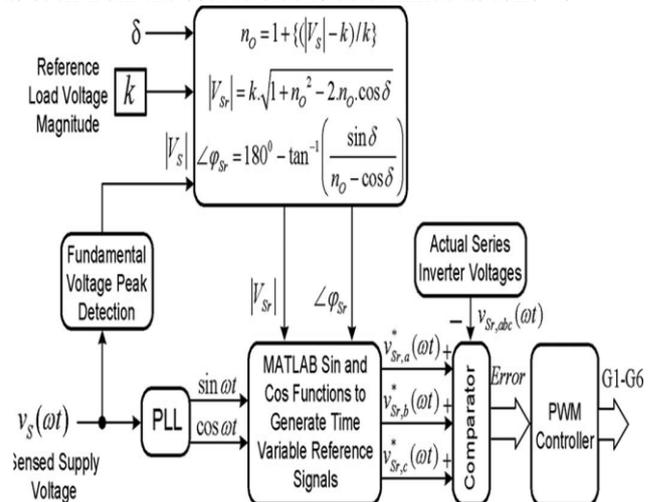


Fig. 11 Reference voltage signal generation for the series inverter of the Proposed UPQC-S approach.

For active power

$$P_{Sh}' = V_S' \cdot I_{Sh}'' \cdot \cos \varphi_{Sh-S}''. \quad (45)$$

From Fig. 8

$$P_{Sh}' = n_o \cdot k \cdot I_{Sh}'' \cdot (-\sin \rho) \quad (46)$$

$$P_{Sh,PAC} = -\frac{(k \cdot I_L) \cdot (\cos \beta - k_o \cdot \cos \varphi_L)}{k_o}. \quad (47)$$

For reactive power

$$Q_{Sh}' = V_S' \cdot I_{Sh}'' \cdot \sin \varphi_{Sh-S}''. \quad (48)$$

From Fig. 8

$$Q_{Sh}' = n_o \cdot k \cdot I_{Sh}'' \cdot \cos(\rho) \quad (49)$$

$$Q_{Sh,PAC} = \frac{(k \cdot I_L) \cdot (\sin \beta)}{k_o}. \quad (50)$$

Using (47) and (50), the active and reactive power flow through shunt inverter of UPQC-S during voltage sag/swell condition can be calculated and utilized to determine the overall UPQC-S VA loading.

(iii) UPQC-S CONTROLLER

A detailed controller for UPQC based on PAC approach is described. Furthermore, the power angle δ is maintained at constant value under different operating conditions. Therefore, the reactive power shared by the series inverter and hence by the shunt inverter changes as given by (43) and (51).

MATLAB CIRCUITS

Below figure 12 gives the simulink diagram of proposed method.



UPQC-S: A Novel Concept of Simultaneous Voltage Sag/Swell and Load Reactive Power Compensations Utilizing Series Inverter of UPQC

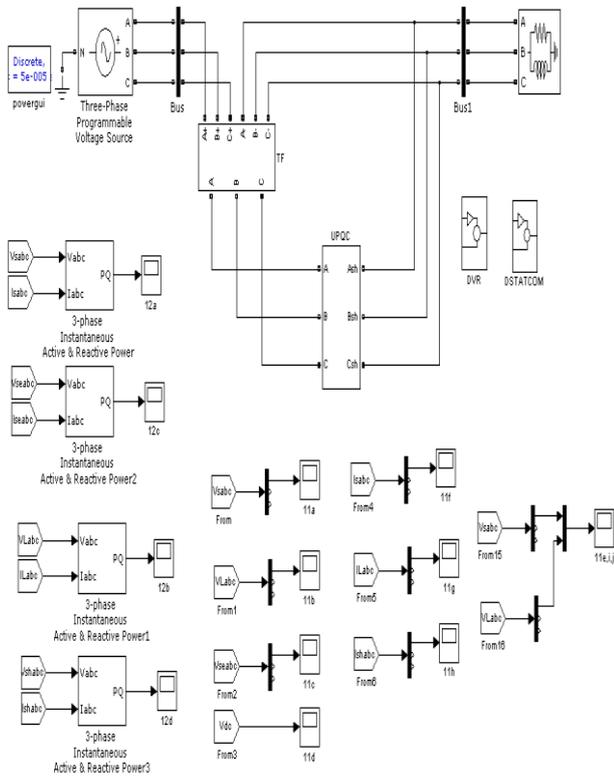
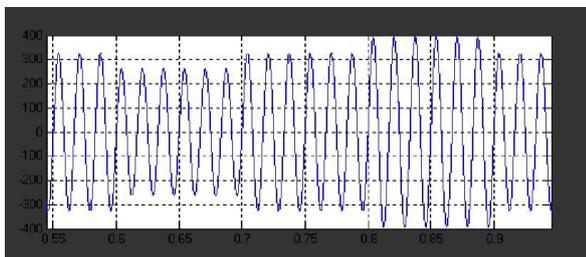


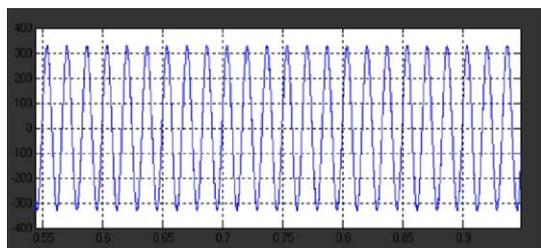
Fig 12

V. RESULTS

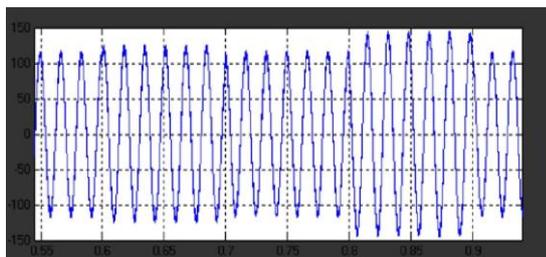
6.1 Simulation results: performance of the proposed UPQC-S approach under voltage sags and swells conditions



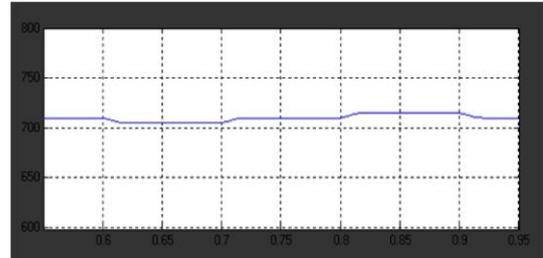
(a) Supply voltage



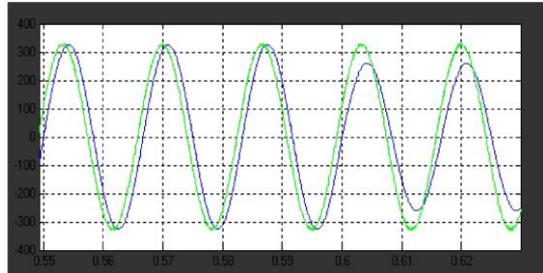
(b) Load voltage



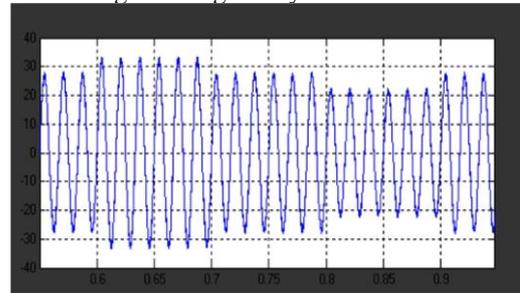
(c) Series inverter injected voltage



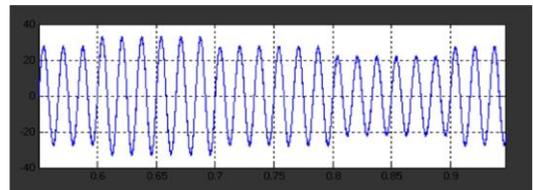
(d) Self-supporting dc bus voltage.



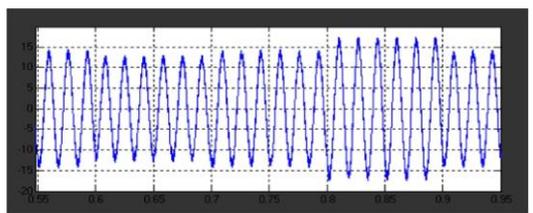
e) Enlarged power angle δ relation between supply and load voltages during steady-state condition.



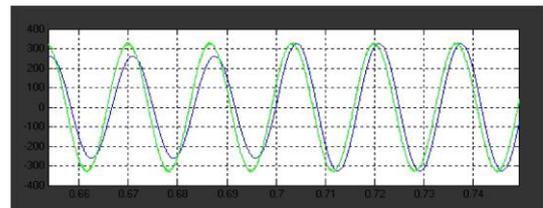
(f) Supply current.



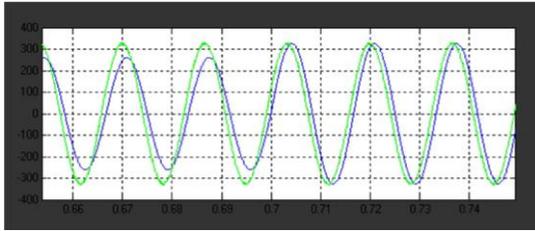
(g) Load current



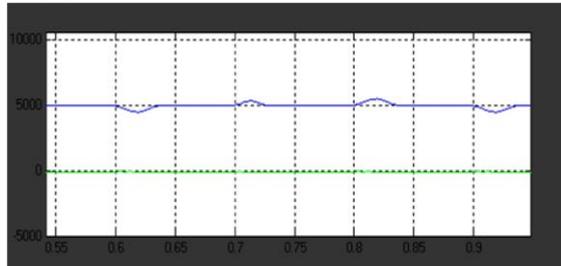
(h) Shunt inverter injected current



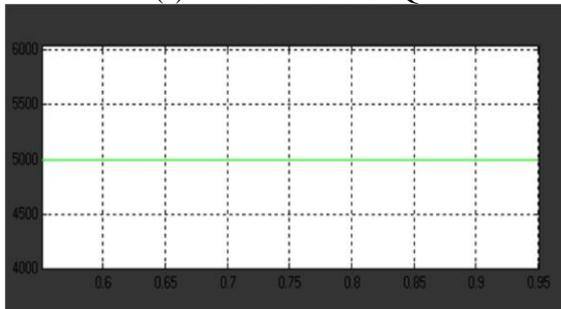
(i) Enlarged power angle δ during voltage sag condition.



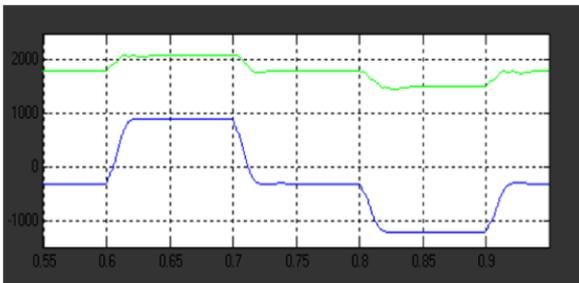
(j) Enlarged power angle δ during voltage swell condition.
Fig. 6.2. Simulation results: active and reactive power flow through source, load, shunt, and series inverter utilizing proposed UPQC-S approach under voltage sag and swell conditions.



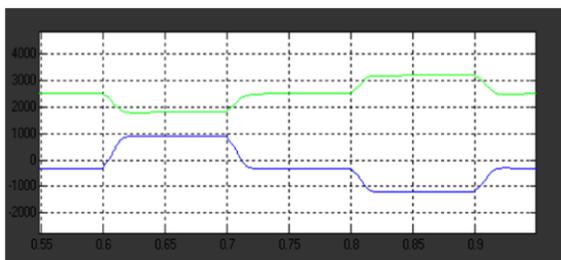
(a) Source P and Q



(b) Load P and Q.



(c) Series inverter P and Q.



(d) Shunt inverter P and Q.

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