

# An Overview of Dynamic Voltage Restorer for Voltage Profile Improvement

M.Sharanya, B.Basavaraja , M.Sasikala

*Abstract - The use of sensitive electronic equipment has increased now a days which has lead to power quality problems. The various power quality disturbances are transients, interruptions, voltage sag, voltage swell, voltage collapse, harmonics etc. To solve these power quality problems various custom power devices are used. Dynamic voltage restorer (DVR) is a custom power device used for the Compensation of voltage sag and swell. In this paper an overview of DVR, its components, functions, compensating strategies and control methods are reviewed in detail and the compensating strategies are compared.*

**Keywords:** Power quality, Dynamic voltage restorer, compensating strategies, control methods.

## I. INTRODUCTION

The various power quality problems are due to the increasing use of non linear and power electronic loads. Harmonics and voltage distortion occur due to these loads. The power quality problems can cause malfunctioning of sensitive equipments, protection and relay system[1]. Distribution system is mainly affected by voltage sag and swell power quality issue. Short circuits, lightning strokes, faults and inrush currents are the causes of voltage sags. Start/stop of heavy loads, badly dimensioned power sources, badly regulated transformers, single line to ground fault on the system lead to voltage swells. Voltage sag is a decrease of the normal voltage level between 10 and 90% of the nominal rms voltage at the power frequency, for durations of 0,5 cycle to 1 minute. Voltage swells are momentary increase of the voltage, at the power frequency, outside the normal tolerances, with duration of more than one cycle and typically less than a few seconds[2]. The use of custom power devices is one of the most efficient method to mitigate voltage sag and swells. There are many custom power devices. Each of which has its own benefits and limitations[6]. Custom power device(CPD) is a powerful tool based on semiconductor switches concept to protect sensitive loads if there is a disturbance from power line. Among the several novel CPD, the Dynamic Voltage Restorer (DVR) are now becoming more established in industry to mitigate the impact of voltage disturbances on sensitive loads.

**Manuscript received on December, 2012.**

**M.Sharanya**, Associate professor, Department of EEE, Vignana Bharathi Institute of Technology, Ghatkesar, Hyderabad, India.

**Dr B.Basavaraja**, HOD & Professor, EEE Department, GITAM University Hyderabad Campus, Hyderabad, India.

**Dr M.Sasikala**, Professor, EEE Department, CVR College of Engineering, Ibrahimpatnam, Hyderabad, India.

Power quality in the distribution system can be improved by using a custom power device DVR for voltage disturbances such as voltage sags, swells, harmonics, unbalanced voltage and etc [10]. The Dynamic Voltage Restorer (DVR) is a device that detects the sag or swell and connects a voltage source in series with the supply voltage in such a way that the load voltage is kept inside the established tolerance limits[15] It is normally installed in a distribution system between the supply and the critical load feeder at the point of common coupling (PCC). Other than voltage sags and swells compensation, DVR also has added other features like: line voltage harmonics compensation, reduction of transients in voltage and fault current limitations.

## II. DYNAMIC VOLTAGE RESTORER

Dynamic voltage restorer is a static var device that has applications in a variety of transmission and distribution systems. It is a series compensation device, which protects sensitive electric load from power quality problems such as voltage sags, swells, unbalance and distortion through power electronic controllers that use voltage source converters (VSC). The first DVR was installed in North America in 1996 - a 12.47 kV system located in Anderson, South Carolina. Since then, DVRs have been applied to protect critical loads in utilities, semiconductor and food processing. Today, the dynamic voltage restorer is one of the most effective PQ devices in solving voltage sag problems.

The basic principle of the dynamic voltage restorer is to inject a voltage of required magnitude and frequency, so that it can restore the load side voltage to the desired amplitude and waveform even when the source voltage is unbalanced or distorted. Generally, it employs a gate turn off thyristor (GTO) solid state power electronic switches in a pulse width modulated (PWM) inverter structure. The DVR can generate or absorb independently controllable real and reactive power at the load side. In other words, the DVR is made of a solid state DC to AC switching power converter that injects a set of three phase AC output voltages in series and synchronism with the distribution line voltages.

Dynamic voltage restorer is a series connected device designed to maintain a constant RMS voltage across a sensitive load[12]. The structure of DVR is shown in Fig.1.

# An Overview of Dynamic Voltage Restorer for Voltage Profile Improvement

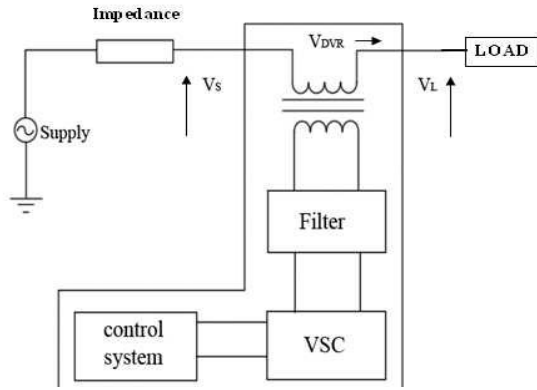


Fig:1 Schematic diagram of DVR

The DVR consists of:

## A. Voltage Source Converter (VSC):

Voltage Source Converter converts the dc voltage from the energy storage unit to a controllable three phase ac voltage. The inverter switches are normally fired using a sinusoidal Pulse Width Modulation scheme.

## B. Injection Transformer:

Injection transformers used in the DVR plays a crucial role in ensuring the maximum reliability and effectiveness of the restoration scheme. It is connected in series with the distribution feeder.

## C. Passive Filters:

Passive Filters are placed at the high voltage side of the DVR to filter the harmonics. These filters are placed at the high voltage side as placing the filters at the inverter side introduces phase angle shift which can disrupt the control algorithm.

## D. Energy storage device/ Control system:

Examples of energy storage devices are dc capacitors, batteries, super-capacitors, superconducting magnetic energy storage and flywheels. The capacity of energy storage device has a big impact on the compensation capability of the system. Compensation of real power is essential when large voltage sag occurs.

## III. VOLTAGE INJECTION METHODS

Voltage injection or compensation methods by means of a DVR depend upon the limiting factors such as; DVR power ratings, various conditions of load, and different types of voltage sags. Some loads are sensitive towards phase angle jump and some are sensitive towards change in magnitude and others are tolerant to these. Therefore the control strategies depend upon the type of load characteristics. There are four different methods of DVR voltage injection which are

- i. Pre-sag compensation method
- ii. In-phase compensation method
- iii. In-phase advanced compensation method
- iv. Voltage tolerance method with minimum energy injection

### A. Pre-Sag/Dip Compensation :

The pre-sag method tracks the supply voltage continuously and if it detects any disturbances in supply voltage it will inject the difference voltage between the sag or voltage at PCC and pre-fault condition, so that the load voltage can be restored back to the pre-fault condition. Compensation of voltage sags in the both phase angle and amplitude sensitive loads would be achieved by pre-sag compensation method. In this method the injected active power cannot be controlled and it is determined by external conditions such as the type of faults and load conditions.

$$VDVR = V_{prefault} - V_{sag}$$

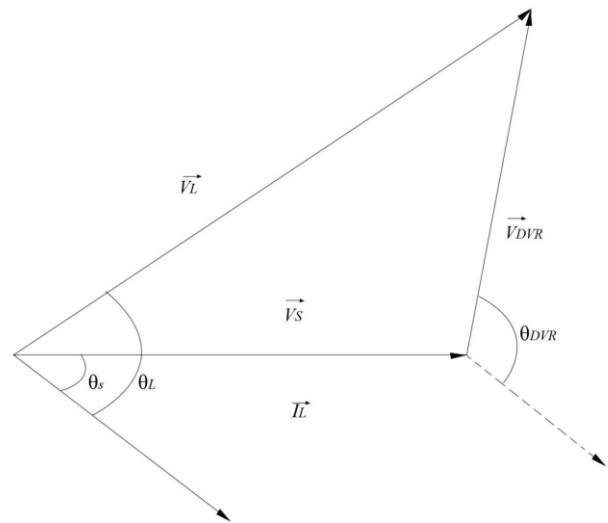


Fig2 : Pre sag compensation

### B. In phase Compensation method :

This is the most straight forward method. In this method the injected voltage is in phase with the supply side voltage irrespective of the load current and pre-fault voltage. The phase angles of the pre-sag and load voltage are different but the most important criteria for power quality that is the constant magnitude of load voltage are satisfied. One of the advantages of this method is that the amplitude of DVR injection voltage is minimum for a certain voltage sag in comparison with other strategies.

**C. In Phase advanced compensation :**

In this method the real power spent by the DVR is decreased by minimizing the power angle between the sag voltage and load current. In case of pre-sag and in-phase compensation method the active power is injected into the system during disturbances. The active power supply is limited stored energy in the DC links and this part is one of the most expensive parts of DVR. The minimization of injected energy is achieved by making the active power component zero by having the injection voltage phasor perpendicular to the load current phasor. In this method the values of load current and voltage are fixed in the system so we can change only the phase of the sag voltage. IPAC method uses only reactive power and unfortunately, not all the sags can be mitigated without real power, as a consequence, this method is only suitable for a limited range of sags.

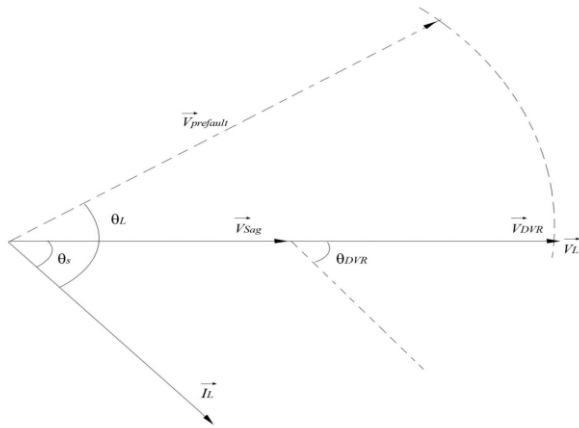
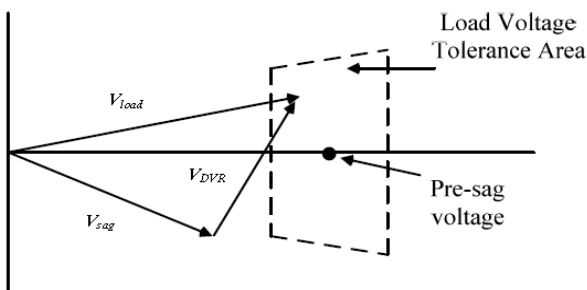


Fig 3 : In Phase compensation method

**D. Voltage tolerance method with minimum energy injection:**

A small drop in voltage and small jump in phase angle can be tolerated by the load itself. If the voltage magnitude lies between 90%-110% of nominal voltage and 5%-10% of nominal state that will not disturb the operation characteristics of loads. Both magnitude and phase are the control parameter for this method which can be achieved by small energy injection.



**IV. CONTROL TECHNIQUES**

**A. Linear controllers :**

The three main voltage controllers, which have been proposed in literature, are Feedforward (open loop), Feedback (closed loop) and Multi-loop controller [13]. The feed-forward voltage

controller is the primary choice for the DVR, because of its simplicity and fastness. The supply voltage is continuously monitored and compared with a reference voltage; if the difference exceeds a certain tolerance, the DVR injects the required voltage. The drawback of the open loop controller is the high steady state error. In the feedback control, the load voltage is measured and compared with the reference voltage, the missing voltage is supplied by the DVR at the supply bus in a feedback loop. This controller has the advantage of accurate response, but it is complex and time-delayed. Multi-loop control is used with an outer voltage loop to control the DVR voltage and an inner loop to control the load current. This method has the strengths of feed-forward and feedback control strategies, on the expense of complexity and time delay.

**B. Non-linear Controllers:**

It appears that the nonlinear controller is more suitable than the linear type since the DVR is truly a non-linear system due to the presence of power semiconductor switches in the inverter bridge. The most non-linear controllers are the Artificial Neural Networks (ANN), Fuzzy Logic (FL) and Space Vector Pulse Width Modulation (SVPWM) [16]. ANN control method has adaptive and self-organization capacity. The ANN has inherent learning capability that can give improved precision by interpolation. FL controllers are an attractive choice when precise mathematical formulations are not possible. When a FL controller is used, the tracking error and transient overshoots of PWM can be considerably reduced. SVPWM control strategy is to adopt a space vector of the inverter voltage to get better performance of the exchange is gained in low switching frequency conditions.

**V. CONCLUSION**

A review of performance of DVR is presented in this paper. By the use of different control techniques it is viewed that DVR is suitable for voltage sag and swell mitigation. The basic structure of DVR, its operation, compensation methods and control techniques are discussed in detail. DVR has the advantage of low cost, require less computational efforts and its control is simple as compared to other methods. DVR provides simpler implementation for voltage profile improvement. Linear controllers provide simpler operation and less computational efforts when compared to other methods.

**REFERENCES**

- [1]. Anita Pakharia, Manoj Gupta "DYNAMIC VOLTAGE RESTORER FOR COMPENSATION OF VOLTAGE SAG AND SWELL: A LITERATURE REVIEW" International Journal of Advances in Engineering & Technology, Vol. 4, Issue 1, pp. 347-355, July 2012.
- [2]. A. de Almeida, L. Moreira, J. Delgado, "Power Quality Problems and New Solutions"
- [3]. M.A.Taghikhani, "Multi-Loop Control System Design for Phase Advanced Dynamic Voltage Restorer" International Journal of Automation & Power Engineering, 1: 20-27, April 2012.
- [4]. Lin Xu1, Yang Han, "Effective Controller Design for the Dynamic Voltage Restorer (DVR) for Voltage Sag Mitigation in Distribution Utilities" ELEKTROTEHNIŠKI VESTNIK 78(5): 304-311, 2011.
- [5]. Rosli Omar and Nasrudin Abd Rahim, "MITIGATION OF VOLTAGE SAGS/SWELLS USING DYNAMIC VOLTAGE RESTORER

- (DVR)” ARPN Journal of Engineering and Applied Sciences, VOL. 4, NO. 4, JUNE 2009.
- [6]. Chellali BENACHAIBA, Brahim FERDI, “Voltage Quality Improvement Using DVR” Electrical Power Quality and Utilisation, Journal Vol. XIV, No. 1, 2008.
  - [7]. V.J. Gosbell “Unresolved Problems in Power Quality”.
  - [8]. C. Benachaiba and B. Ferdi, “Power Quality Improvement Using DVR” American Journal of Applied Sciences 6 (3): 396-400, 2009.
  - [9]. Shairul Wizmar Wahab and Alias Mohd Yusof, “ Voltage Sag and Mitigation Using Dynamic Voltage Restorer (DVR) System” ELEKTRIKA, VOL. 8, NO. 2, 2006, 32-37.
  - [10]. Rosli Omar, N.A. Rahim and Marizan Sulaiman, “Dynamic Voltage Restorer Application for Power Quality Improvement in Electrical Distribution System: An Overview” Australian Journal of Basic and Applied Sciences, 5(12): 379-396, 2011.
  - [11]. Sachin V. Rajani, Dr. R.C. Jha, Prem Prakash, “DEEPER AND EXTENDED VOLTAGE SAG MITIGATION BY DYNAMIC VOLTAGE RESTORER INTERFACED WITH ULTRA CAPACITOR” International Journal of Advanced Engineering Technology, Vol.III/ Issue I/January-March, 2012/230-233.
  - [12]. Dr. K RAMA SUDHA, K PADMAVATHI, “MODELING AND SIMULATION OF DYNAMIC VOLTAGE RESTORER (DVR) USING NEURO FUZZY INFERENCE SYSTEM” International Journal of Engineering Science and Technology, Vol. 4 No.03 March 2012.
  - [13]. Mahmoud A. El-Gammal, Amr Y. Abou-Ghazala, and Tarek I. El-Shennawy, “Dynamic Voltage Restorer (DVR) for Voltage Sag Mitigation” International Journal on Electrical Engineering and Informatics - Volume 3, Number 1, 2011.
  - [14]. Mr. Subhro Paul, Pradip Kumar Saha, Gautam Kumar Panda, “Power Quality Improvement Using New Control Algorithm Based Dynamic Voltage Restorer” International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 1, Issue 3, September 2012.
  - [15]. M. E. C. Brito, M. C. Cavalcanti, L. R. Limongi and F. A. S. Neves, “Low Cost Dynamic Voltage Restorer” International Conference on Renewable Energies and Power Quality, 28th to 30th March, 2012.
  - [16]. B. Rajani, P. Sangameswara Raju, “ Enhancement of Power Quality by Optimal Placement of Dstatcom For Voltage Sag Mitigation Using ANN Based Approach” International Journal of Soft Computing and Engineering (IJSCE) ISSN: 2231-2307, Volume-2, Issue-3, July 2012.
  - [17]. T. Devaraju, Dr V.C. Veera Reddy, Dr M. Vijay kumar “ Modelling and simulation of custom power devices to mitigate power quality problems” International Journal of Engineering Science and Technology Vol. 2(6), 2010, 1880-1885
  - [18]. Shairul Wizmar Wahab and Alias Mohd Yusof “ Voltage Sag and Mitigation Using Dynamic Voltage Restorer (DVR) System” ElektriKA, VOL. 8, NO. 2, 2006, 32-37
  - [19]. Rosli Omar and Nasrudin Abd Rahim “MITIGATION OF VOLTAGE SAGS/SWELLS USING DYNAMIC VOLTAGE RESTORER (DVR)” ARPN Journal of Engineering and Applied Sciences, VOL. 4, NO. 4, JUNE 2009
  - [20]. Chellali BENACHAIBA, Brahim FERDI “Voltage Quality Improvement Using DVR” Electrical Power Quality and Utilisation, Journal Vol. XIV, No. 1, 2008



**Dr. M. Sasikala<sup>3</sup>** was born in 1962. She is a member of IEEE. She has obtained her BE and ME from Osmania college of Engineering, Osmania University, Hyderabad, India. She has obtained her Doctorate degree from Jawaharlal Nehru Technological University Hyderabad in 2008. Presently she is working as Professor in CVR college of Engineering, Andhra Pradesh. Her areas of interest include Electrical machines, sensors and Fuzzy logic controllers.



**M. Sharanya<sup>1</sup>** was born in 1981. She is a member of IEEE. She has obtained her BTech and Mtech degree from Jawaharlal Nehru Technological University Hyderabad, INDIA. She is presently working as Associate professor in Vignana Bharathi Institute of Technology, Ghatkesar, Hyderabad. Her areas of interest are power electronics, power semiconductor drives, Electrical machines and power quality.



**Dr. Basavaraja Banakara<sup>2</sup>** was born in 1970. He is senior IEEE member SINCE 2005. He Obtained his B.Tech (EEE) Degree from Gulbarga University and M.Tech from Karnataka University, INDIA. He obtained his Doctorate degree from National Institute of Technology Warangal in 2007. Presently he is working as Vice-Principal at GITAM University Hyderabad Campus, Andhra Pradesh. His areas of interest includes Adjustable Speed drives, high voltage Engineering, MICROGRIDS and EMTP applications.